Ultra-Fast Silicon Detector

- The "4D" challenge
- A parameterization of time resolution
- The "Low Gain Avalanche Detectors" project
- Laboratory measurements
- UFSD: LGAD optimized for timing measurements
- WeightField2: a simulation program to optimize UFSD
- First measurements
- Future directions

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The 4D challenge

Is it possible to build a detector with concurrent excellent time and position resolution?

Can we provide in the same detector and readout chain:

- Ultra-fast timing resolution [~10 ps]
- Precision location information [10's of μ m]

State-of-the-art Timing Detectors

Timing detectors exploit very fast physics processes such as

Cherenkov light emission or electronic avalanches to create prompt signals



- These detectors measure time very accurately but locate particles with the **precision of ~ 1 mm**
- Good timing is obtain by using a gain mechanism, either in the detector or in the electronics

State-of-the-art Position Detectors

Extremely good position detectors are currently in use in every major high energy physics experiment:

- Millions of channels
- Very reliable
- Very radiation hard

The timing capability is however limited to ~ 100-150 ps (NA62 @CERN)



σ_t ~ 100-150 ps σ_x ~ 20-30 μm



Time is set when the signal crosses the comparator threshold

The timing capabilities are determined by the characteristics of the signal at the output of the pre-Amplifier and by the TDC binning.

Noise source: Time walk and Time jitter

Time walk: the voltage value V_{th} is reached at different times by signals of different amplitude



Jitter: the noise is summed to the signal, causing amplitude variations



Due to the physics of signal formation

Mostly due to electronic noise

$$\sigma_{\text{Total}}^2 = \sigma^2_{\text{Time Walk}} + \sigma_{\text{Jitter}}^2 + \sigma_{\text{TDC}}^2$$

Time Resolution and slew rate

Using the expressions in the previous page, we can write



where:

- $S/t_r = dV/dt = slew rate$
- N = system noise
- $V_{th} = 10 N$

Assuming constant noise, to minimize time resolution **we need to maximize the S/t_r term** (i.e. the slew rate dV/dt of the signal)

→ We need large and short signals

Signal formation in silicon detectors

We know we need a large signal, but how is the signal formed?

What is controlling the slew rate?

 $\frac{\mathrm{dV}}{\mathrm{dt}} \propto ?$



A particle creates charges, then:

- The charges start moving under the influence of an external field
- The motion of the charges induces a current on the electrodes
- The signal ends when the charges reach the electrodes

How to make a **good** signal

Signal shape is determined by Ramo's Theorem:



A key to good timing is the uniformity of signals:

Drift velocity and Weighting field need to be as uniform as possible

Good signals for Timing



- → Large pad to have uniform weighting field
- → Lot's of charge

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Non-Uniform Energy deposition

Landau Fluctuations cause two major effects:

- Amplitude variations, that can be corrected with time walk compensation
- For a given amplitude, the charge deposition is non uniform.

These are 3 examples of this effect:



What is the signal of one e/h pair?

(Simplified model for pad detectors)

Let's consider **one single electron-hole pair**.

The integral of their currents is equal to the electric charge, q:

$$\int [i_{el}(t) + i_{h}(t)] dt = q$$

However the shape of the signal depends on the thickness d:

thinner detectors have higher slew rate



Large signals from thick detectors?

(Simplified model for pad detectors)

Thick detectors have higher number of

charges:

$$Q_{tot} \sim 75 q^*d$$

However each charge contributes to the initial current as: $i \propto q v \frac{1}{d}$

The initial current for a silicon detector does not depend on how thick (d) the sensor is:

$$i = Nq \frac{k}{d} v = (75dq) \frac{k}{d} v = 75kqv \sim 1 - 2*10^{-6} A$$
Number of e/h = 75/micron
Weighting field
Velocity







Best result : NA62, 150 ps on a 300 x 300 micron pixels

How can we do better?

Possible approaches

We need to minimize this expression:

$$\sigma_t^2 = ([\frac{V_{th}}{S/t_r}]_{RMS})^2 + (\frac{N}{S/t_r})^2$$

- **APD** (silicon with gain ~ 100): maximize S
 - Very large signal
- Diamond: minimize N, minimize t_r
 - Large energy gap, very low noise, low capacitance
 - Very good mobility, short collection time t_r
- LGAD (silicon with gain ~ 10): minimize N, moderate S
 - Low gain to avoid shot noise and excess noise factor

The APD approach

The key to this approach is the large signal: if your signal is large enough, everything becomes easy.



- Excellent time resolution
- Good radiation resistance up to $< 10^{14}$ neg/cm²
- They will propose a system for the CT-PPS

See:

https://indico.cern.ch/event/363665/contribution/7/material/slides/0.pdf

The Diamond approach

Diamond detectors have small signal: two ways of fighting this problem



The "Low-Gain Avalanche Detector" approach

Is it possible to manufacture a silicon detector that looks like a normal pixel or strip sensor, but with a much larger signal (RD50)?

- 750 e/h pair per micron instead of 75 e/h?
- Finely Segmented
- Radiation hard
- No dead time
- Very low noise (low shot noise)
- No cross talk
- Insensitive to single, low-energy photon

Many applications:

- Low material budget (signal in 30 micron == signal 300 micron)
- Excellent immunity to charge trapping (larger signal, shorter drift path)
- Very good S/N: 5-10 times better than current detectors
- Good timing capability (large signal, short drift time)

Gain in Silicon detectors

Gain in silicon detectors is commonly achieved in several types of sensors. It's based on the avalanche mechanism that starts in high electric fields: **E ~ 300 kV/cm**

Charge multiplication

Gain:

- α = strong E dependance
- $\alpha \sim 0.7$ pair/ μ m for electrons,
- $\alpha \sim 0.1$ for holes

 $N(l) = N_0 \cdot e^{\alpha \cdot l}$ $G = e^{\alpha \cdot l} \quad \alpha_{e,h}(E) = \alpha_{e,h}(\infty) * \exp\left(-\frac{b_{e,h}}{|E|}\right)$



Concurrent multiplication of electrons and holes generate very high gain

Silicon devices with gain:

- APD: gain 50-500
- SiPM: gain ~ 10⁴

How can we achieve E ~ 300kV/cm?

1) Use external bias: assuming a 300 micron silicon detector, we need $V_{\text{bias}} = 30 \text{ kV}$

Not possible



2) Use Gauss Theorem:

$$\sum q = 2\pi r * E$$



E = 300 kV/cm → q ~ 10¹⁶ /cm³

Low Gain Avalanche Detectors (LGADs)

The LGAD sensors, as proposed and manufactured by CNM

(National Center for Micro-electronics, Barcelona):

High field obtained by adding an extra doping layer

E ~ 300 kV/cm, closed to breakdown voltage





Sensor: Simulation

We developed a full sensor simulation to optimize the sensor design

WeightField2, F. Cenna, N. Cartiglia 9th Trento workshop, Genova 2014 Available at http://personalpages.to.infn.it/~cartigli/weightfield2

It includes:

- Custom Geometry
- Calculation of drift field and weighting field
- Currents signal via Ramo's Theorem
- Gain
- Diffusion
- Temperature effect
- Non-uniformdeposition
- Electronics



WeightField2: a program to simulate silicon detectors



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WeightField2: output currents



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WeightField2: response of the read-out electronics



How gain shapes the signal



Gain electron: absorbed immediately Gain holes: long drift home



Electrons multiply and produce additional electrons and holes.

- Gain electrons have almost no effect
- Gain holes dominate the signal

➔ No holes multiplications

Interplay of gain and detector thickness

The rate of particles produced by the gain does not depend on *d* (assuming saturated velocity v_{sat})

Gain_





Constant rate of production

However the initial value of the **gain current depends on d** (via the weighing field)

$$di_{gain} \propto dN_{Gain} qv_{sat}(\frac{k}{d}) \rightarrow \text{Gain current} \sim 1/d$$

A given value of gain has much more effect on thin detectors



Significant improvements in time resolution require thin detectors

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Ultra Fast Silicon Detectors

UFSD are LGAD detectors optimized to achieve the best possible time resolution

Specifically:

- 1. Thin to maximize the slew rate (dV/dt)
- Parallel plate like geometries (pixels..) for most uniform weighting field
- 3. High electric field to maximize the drift velocity
- 4. Highest possible resistivity to have uniform E field
- 5. Small size to keep the capacitance low
- 6. Small volumes to keep the leakage current low (shot noise)

First Measurements and future plans

LGAD laboratory measurements

- Doping concentration
- Gain
- Time resolution measured with laser signals

LGAD Testbeam measurements

- Landau shape at different gains
- Time resolution measured with MIPs

LGAD Sensors in Torino

Thickness: 300 μm





Run	Sensor	P-Layer Implant (E=100 KeV)	Gain	V _{break}	Metal Layer
		-			
6474	W8_B4	?	~ 10	> 500 V	DR
6474	W8_C6	?	~ 10	> 500 V	DC
6474	W9_B6	No implant	No Gain	> 500 V	DR
7062	W1_F3	1.6 x 10 ¹³ cm ⁻²	~ 1-2	> 500 V	DR
7062	W3_H5	2.0 x 10 ¹³ cm ⁻²	~ 10	> 500 V	DR
7062	W7_D7	No implant	No Gain	> 500 V	DR

Doping profile from CV measurement - I



Doping profile from CV measurement - II



Signal amplitude

Using laser signals we are able to measure the different responses of LGAD and traditional sensors



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Gain

The gain is estimated as the ratio of the output signals of LGAD detectors to that of traditional one

The gain increases linearly with Vbias (not exponentially!)



Laser Measurements on CNM LGAD

We use a 1064 nm picosecond laser to emulate the signal of a MIP particle (without Landau Fluctuations)

The signal output is read out by either a Charge sensitive amplifier or a Current Amplifier (Cividec)



Testbeam Measurements on CNM LGAD

In collaboration with Roma2, we went to Frascati for a testbeam using 500 MeV electrons

As measured in the lab, the gain ~ doubles going from 400 -> 800 Volt.





The gain mechanism preserves the Landau amplitude distribution of the output signals

100 GeV pion Testbeam with CNM LGAD

Teasbeam data understood as the sum of 1 MIP, 2 MIP or 3 MIP



Properties of the Landau distributions



Testbeam Measurements on CNM LGAD

Time difference between two LGAD detectors crossed by a MIP

Tested different types of electronics (Rome2 SiGe, Cividec), **Not yet optimized for these detectors**



Present results and future productions

With WF2, we can reproduce very well the laser and testbeam results.

Assuming the same electronics, and 1 mm² LGAD pad with gain 10, we can predict the timing capabilities of the next sets of sensors.



Effect of Landau Fluctuations on the time resolution

The effect of Landau fluctuations in a MIP signal are degrading the time resolution by roughly 30 % with respect of a laser signal



Irradiation tests

The gain decreases with irradiations: at 10¹⁴ n/cm² is 20% lower

→ Due to boron disappearance



What-to-do next:

1) Planned new irradiation runs (neutrons, protons), new sensor geometries

2) Use Gallium instead of Boron for gain layer (in production now)

3) Design the UFSD to have a gain higher than we need, ~ 30 at 500 V.
 - We use UFSD at gain 10-15, at 200 Volt

- When radiation damage lowers the gain, we increase Vbias to compensate

Merging timing with position resolution

Electrode segmentation makes the E field very non uniform, and therefore ruins the timing properties of the sensor



We need to find a geometry that has very uniform E field, while allowing electrode segmentation.

1) Segmentation: buried junction

Separate the multiplication side from the segmentation side



Moving the junction on the deep side allows having a very uniform multiplication, regardless of the electrode segmentation

2) Segmentation: AC coupling



Details of Resistivity and AC coupling

 $R_{Ampl} * C_{detector} \sim 100 \ \Omega * 1 pF \sim 100 \text{ ps}$ R_{Ampl} ŀ€[∙]_{AC} R_{Sheet} Detector Detéctor Detector Detector Detector **Freezing time** $R_{Sheet} * C_{AC} \sim 1k\Omega * 100 pF \sim 100 ns$

Additional Rise time

Only a small part of the detector is involved

Detector

3) Segmentation: splitting gain and position measurements



The ultimate time resolution will be obtained with a custom ASIC. However we might split the position and the time measurements

Electronics

To fully exploit UFSDs, dedicated electronics needs to be designed.

The signal from UFSDs is different from that of traditional sensors





Interplay of T_{Col} and $\tau = R_{in} C_{Det}$



There are two time constants at play:

- T_{col}: the signal collection time (or equivalently the rise time)
- $\tau = R_{in} C_{Det}$: the time needed for the charge to move to the electronics



What is the best "time measuring" circuit?



Constant Fraction Discriminator

The time is set when a fixed fraction of the amplitude is reached

Time over Threshold

The amount of time over the threshold is used to correct for time walk

Multiple sampling

Most accurate method, needs a lot of computing power.

Noise - I



Details of shot noise in LGAD - APD







Low leakage current and low gain (~ 10) together with short shaping time are necessary to keep the noise down.

Next CNM productions



Summer 2015: 50 micron

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Next Steps

- Wafer Production
 200 micron thick sensors by Spring-2015
 100 and 50 micron thick sensors by Summer 2015.
- 2. Production of UFSD doped with Gallium instead of Boron.
- **3.** Study of reversed-UFSD started for the production of pixelated UFSD sensors (FBK, Trento).
- 4. UFSD are included in the CMS TDR CT-PPS as a solution for forward proton tagging
- 5. Use of UFSD in beam monitoring for hadron beam. INFN patent and work on-going
- 6. Interest in UFSD for 4D tracking at high luminosity
- 7. Testbeam analyses just started. Results coming soon...

UFSD – Summary

We are just starting to understand the timing capability of UFSD

- Low-gain avalanche diodes (LGAD) offer silicon sensors with an enhanced signal amplitude: UFSD are LGAD detectors optimized for timing resolution.
- Several options under studies to obtain concurrently excellent space and time resolutions.
- We developed a program, Weightfield2 to simulate the behaviors of LGAD and optimized them for fast timing (available at <u>http://personalpages.to.infn.it/~cartigli/Weightfield2.0/</u>)

Timescale: 1 year to asses UFSD timing capabilities

Presented at IEEE, oral and posters, presentations



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Additional references

Several talks at the 22nd, 23rd and 24th RD50 Workshops:

23rd RD50: https://indico.cern.ch/event/265941/other-view?view=standard 22nd RD50: http://panda.unm.edu/RD50_Workshop/

10th Trento Workshop, Trento, Feb 2015.

9Th Trento Workshop, Genova, Feb 2014.

F. Cenna "Simulation of Ultra-Fast Silicon Detectors"

N. Cartiglia "Timing capabilities of Ultra-Fast Silicon Detector"

<u>Papers:</u>

[1] N. Cartiglia, Ultra-Fast Silicon Detector, 13th Topical Seminar on Innovative Particle and Radiation Detectors (IPRD13), 2014 JINST 9 C02001, <u>http://arxiv.org/abs/1312.1080</u>

[2] H.F.-W. Sadrozinski, N. Cartiglia et al., Sensors for ultra-fast silicon detectors, Proceedings "Hiroshima" Symposium HSTD9, DOI: 10.1016/j.nima.2014.05.006 (2014).

Backup

The "Low-Gain Avalanche Detector" project

Is it possible to manufacture a silicon detector that looks like a normal pixel or strip sensor, but with a much larger signal (RD50)?

- 730 e/h pair per micron instead of 73 e/h
- Finely segmented
- Radiation hard
- No dead time
- Very low noise (low shot noise)
- No cross talk





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How can we progress? Need simulation

We developed a full simulation program to optimize the sensor design, WeightField2, (http://cern.ch/weightfield2)

It includes:

- Custom Geometry
- Calculation of drift field and weighting field
- Currents signal via Ramo's Theorem
- Gain
- Diffusion
- Temperature effect
- Non-uniform deposition
- Electronics

Poster Session IEEE N11-8



Comparison Data Simulation

Alpha from Top







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CNM LGADs mask

CNM, within the RD50 project, manufactured several runs of LGAD, trying a large variety of geometries and designs

This implant controls the value of the gain



Wafer Number	P-layer Implant (E = 100 keV)	Substrate features	Expected Gain
1-2	1.6 × 10 ¹³ cm ⁻²	HRP 300 (FZ; ρ>10 KΩ·cm; <100>; T = 300±10 μm)	2-3
3-4	2.0 × 10 ¹³ cm ⁻²	HRP 300 (FZ; ρ>10 KΩ·cm; <100>; T = 300±10 μm)	8 – 10
5-6	2.2 × 10 ¹³ cm ⁻²	HRP 300 (FZ; ρ>10 KΩ·cm; <100>; T = 300±10 μm)	15
7	() PiN Wafer	HRP 300 (FZ; ρ>10 KΩ·cm; <100>; T = 300±10 μm)	No Gain

Electronics: What is the best pre-amp choice?



Details of AC coupling - II



Drift Velocity



Highest possible E field to saturate velocity
Highest possible resistivity for velocity uniformity



Weighting Field: coupling the charge to the electrode



Strip: 100 µm pitch, 40 µm width



Bad: almost no coupling away from the electrode

Pixel: 300 µm pitch, 290 µm width



Good: strong coupling almost all the way to the backplane

The weighting field needs to be as uniform as possible, so that the coupling is always the same, regardless of the position of the charge

The Diamond approach - II

TOTEM collaboration: couple diamond detector with a tailored front-end and a full digitizing readout (SAMPIC, Switching Capacitor Sampler)

Excellent results at a very recent testbeam with ~ $4.5 \times 4.5 \text{ mm}^2$ detectors

The result allows TOTEM to

their Roman Pot set-up:

introduce timing measurement is

Vertical top pots used for timing



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Noise for Gain = 1 and Gain = 10



The minimum noise value is pushed higher and to a much shorter shaping time:

1000e- at 20 ns with Gain = 1 -> 3000e- @ 1 ns with Gain = 10

→LGADs need very short shaping time ←

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LGAD Optimum S/N: numbers

The noise increases faster than then signal: the ratio S/N becomes worse at higher gain. →There is an Optimum Gain value ←

Let's consider the following situation:

- Signal = 20k e-
- Shaping time 1 ns
- Voltage Noise = 1k e-
- Shot Noise (G = 1) = 10 e- I
- Excess Noise Factor $M^{x} = 0.25, 0.5, 1$

Summary

- 1) For a given ENF, there is an optimum gain
- The optimum gain is a function of the excess noise exponent x: higher x values cause lower optimum gains
- 3) Higher optimum gains require shorter shaping time

