

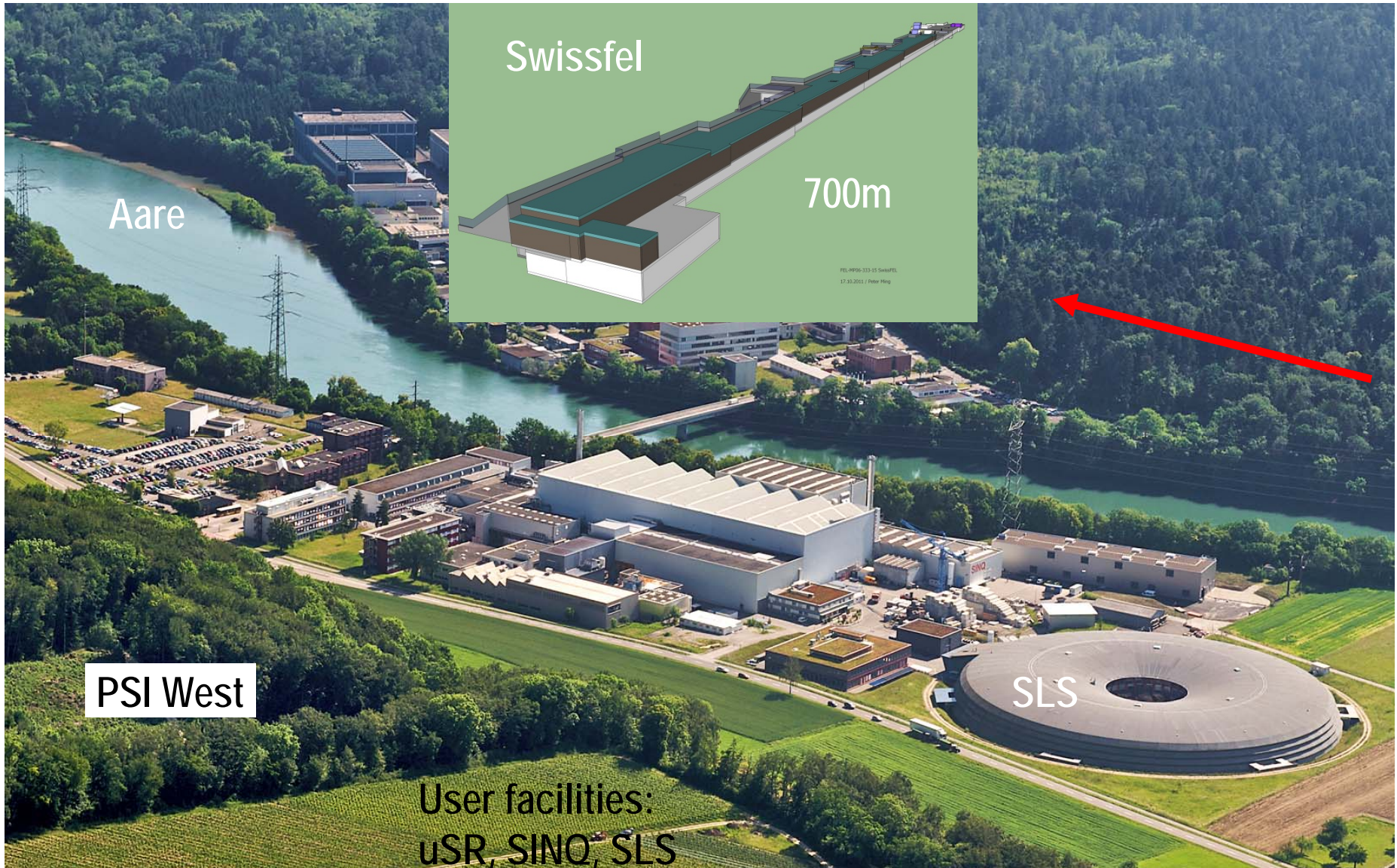


Wir schaffen Wissen – heute für morgen

**Paul Scherrer Institut**

Bernd Schmitt

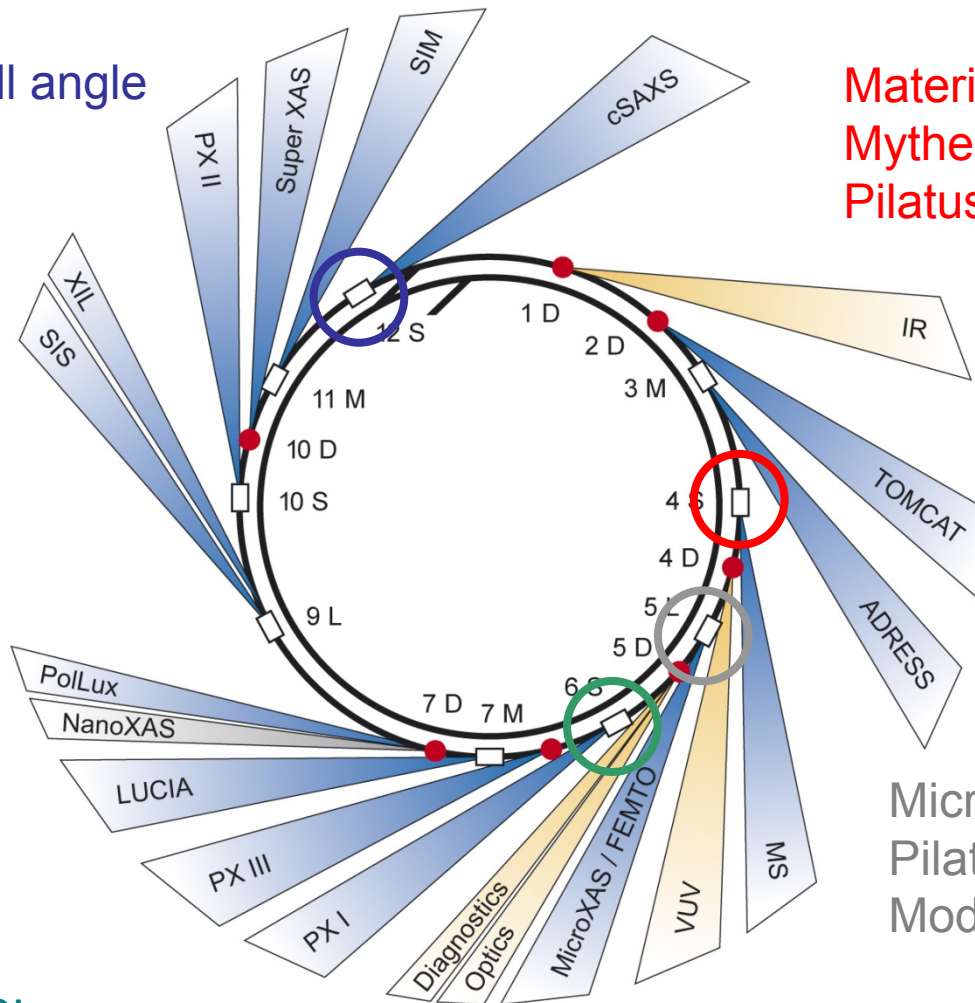
**X-ray Detector Development at the Swiss Light Source**



*~1500 Staff employees; 30Km from Zurich, task in ETH domain: run large scale facilities*

csax beamline:  
Pilatus2M for small angle scattering

Material science beamline:  
Mythen for powder diffraction  
Pilatus for surface diffraction

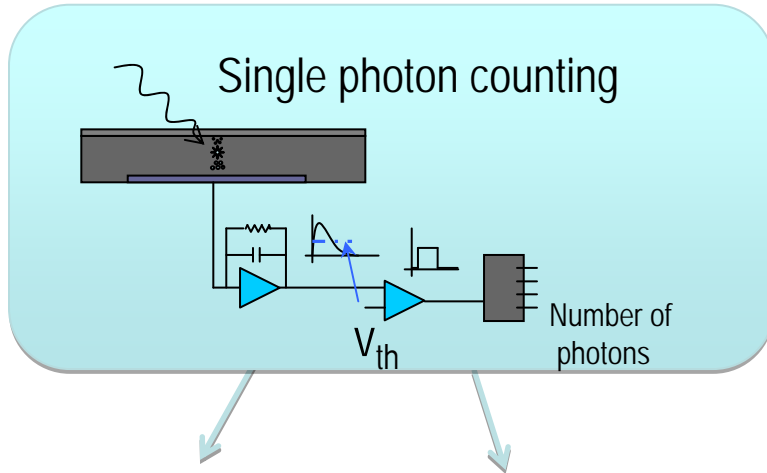


MicroXAS/Femto:  
Pilatus and Mythen single  
Module Systems

PX beamline:  
Pilatus6M for protein  
crystallography

- Operating
- Under construction
- Pilot phase
- Undulator
- Bending magnet

## Synchrotron detectors

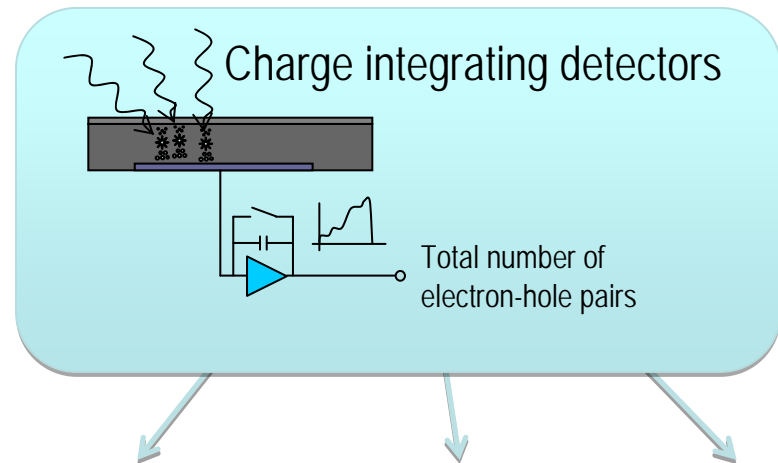


Mythen II

Eiger



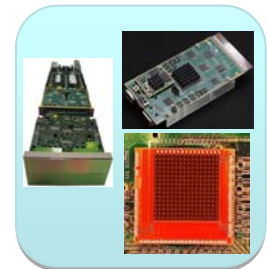
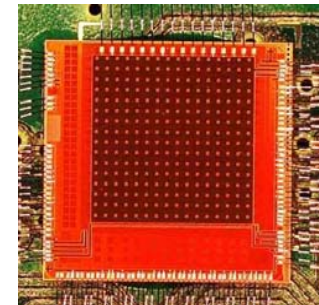
## X-ray free-electron laser detectors



Gotthard

AGIPD

SwissFEL



## Detector principles:

- hybrid detectors
- single photon counting
- charge integrating

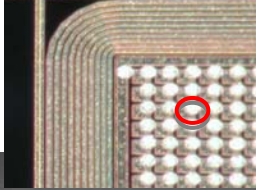
Single Photon counting: Mythen, Eiger

Charge integrating: Gotthard, Swissfel

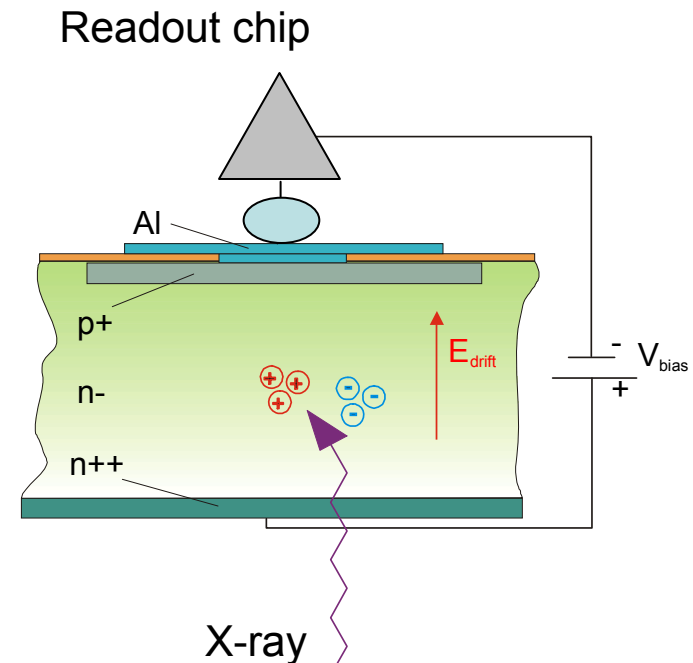
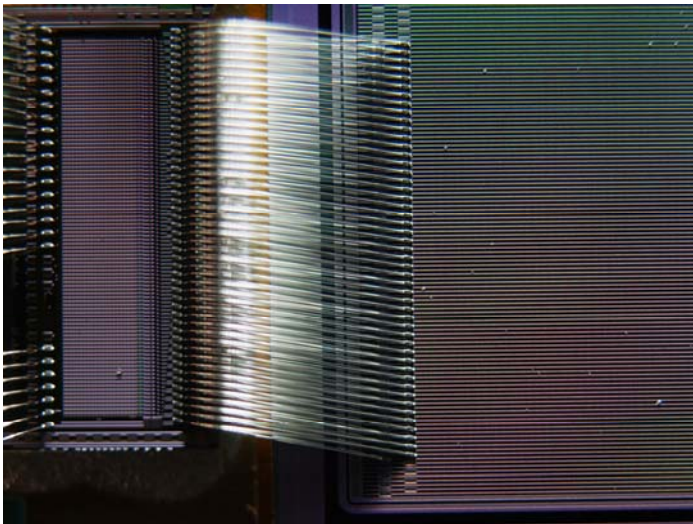
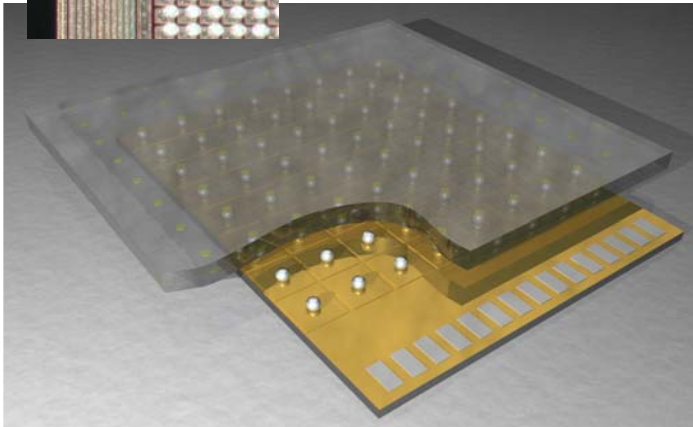
Detector research: What are the limits in pixels size?  
How can one reach the highest position resolution?

# Hybrid Silicon Detectors

2D array of pixels



Silicon sensor



3.6eV per eh pair in Si

Charge for 12keV=3300 electrons = 0.5 fC

## MYTHEN

1k to 30k 50µm strips for powder diffraction, small angle scattering, medical imaging...



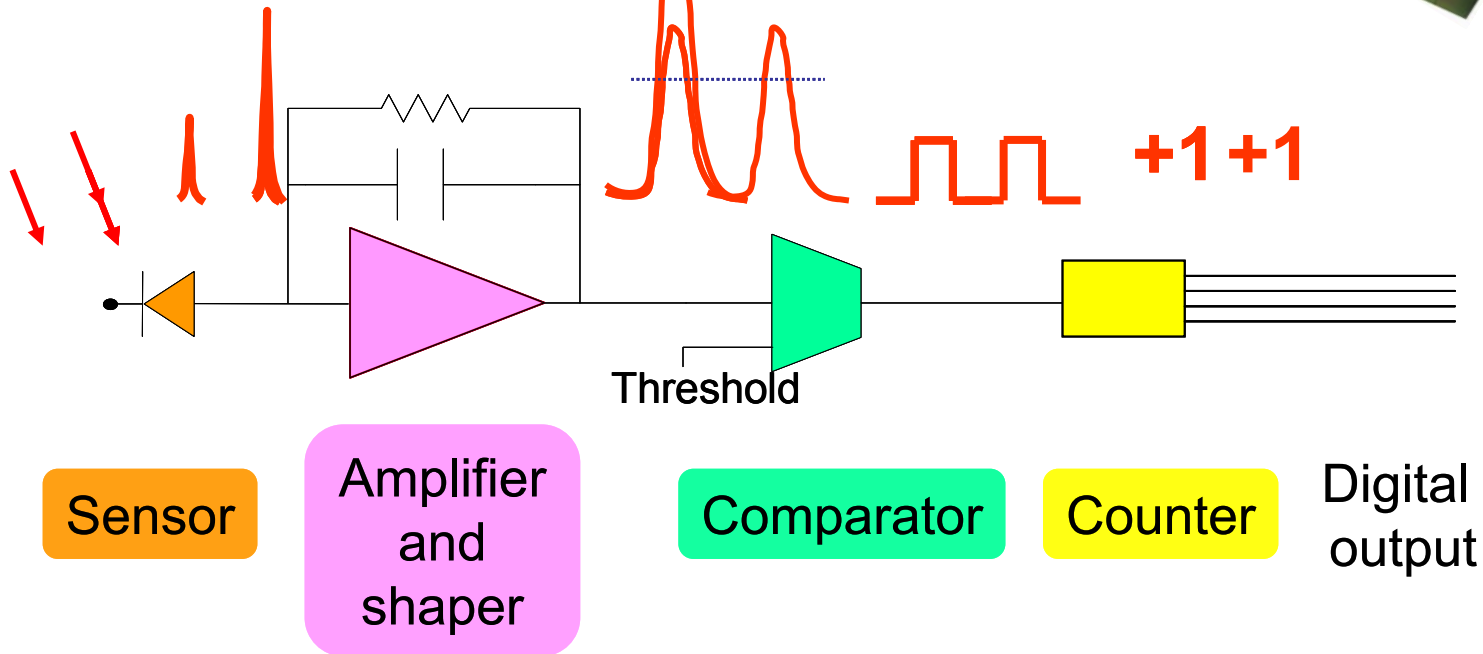
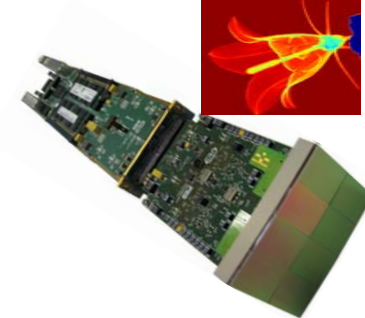
## PILATUS

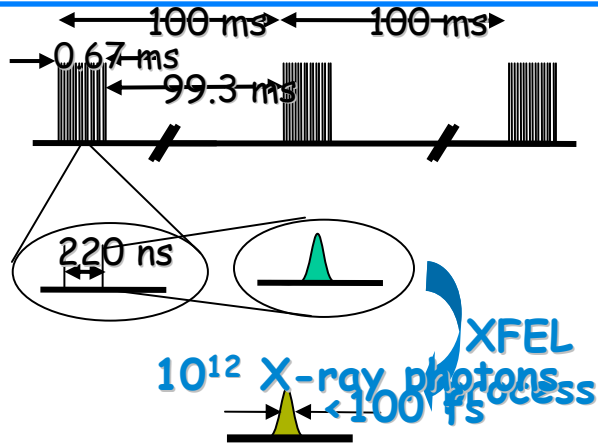
100k to 6M 172µm pixels for protein crystallography, small angle scattering, imaging ...



## EIGER

500k to 9M 75µm pixels, for small angle scattering, CDI, XPCS, protein crystallography, imaging ...





Dynamic range: 10000

- voltage swing usually  $\sim 1\text{V}$
- noise usually  $\sim \text{mV}$

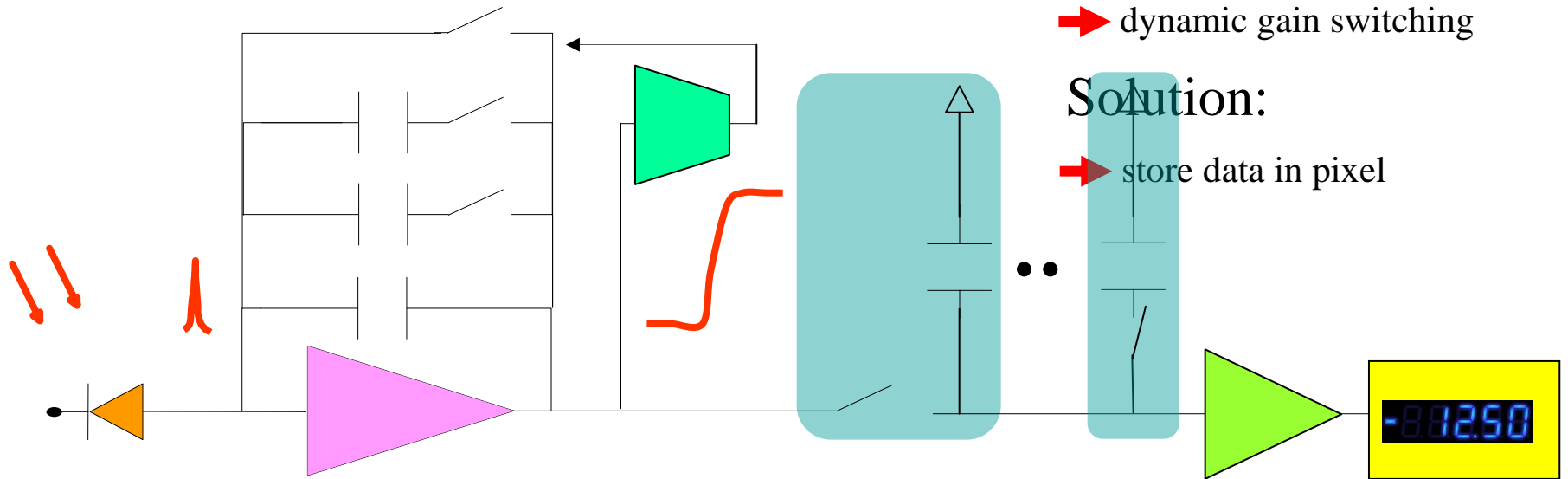
→ 1 photon  $\sim 10\text{mV}$   
100 photons dynamic range

Solution:

→ dynamic gain switching

Solution:

→ store data in pixel



Sensor

Amplifier  
and reset

Gain  
switching

Sample  
and hold

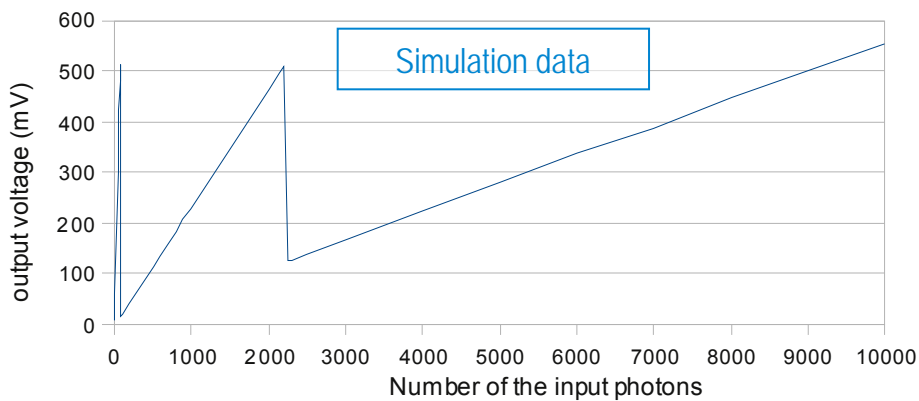
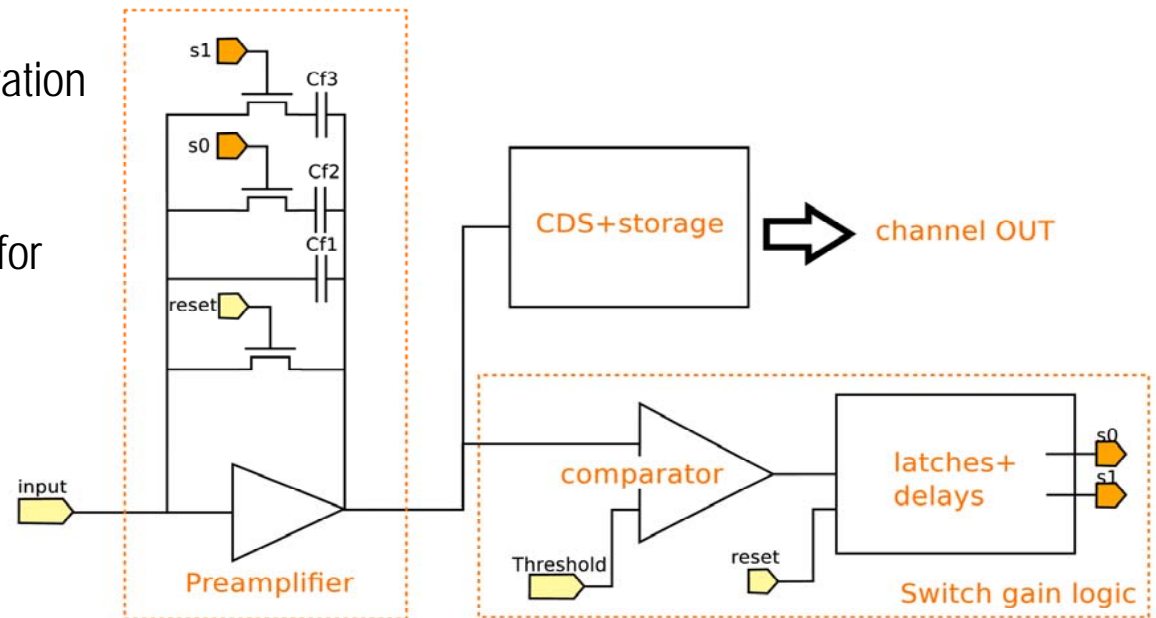
Analog  
output

External  
ADC



# Preamplifier with gain switching

- CSA in charge integrating configuration
- 3 feedback capacitors
- Common for 1D and 2D, baseline for AGIPD, Gotthard and Swissfel

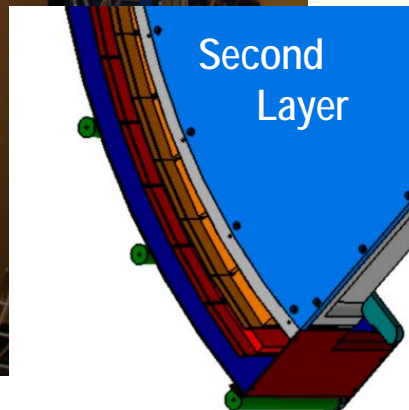
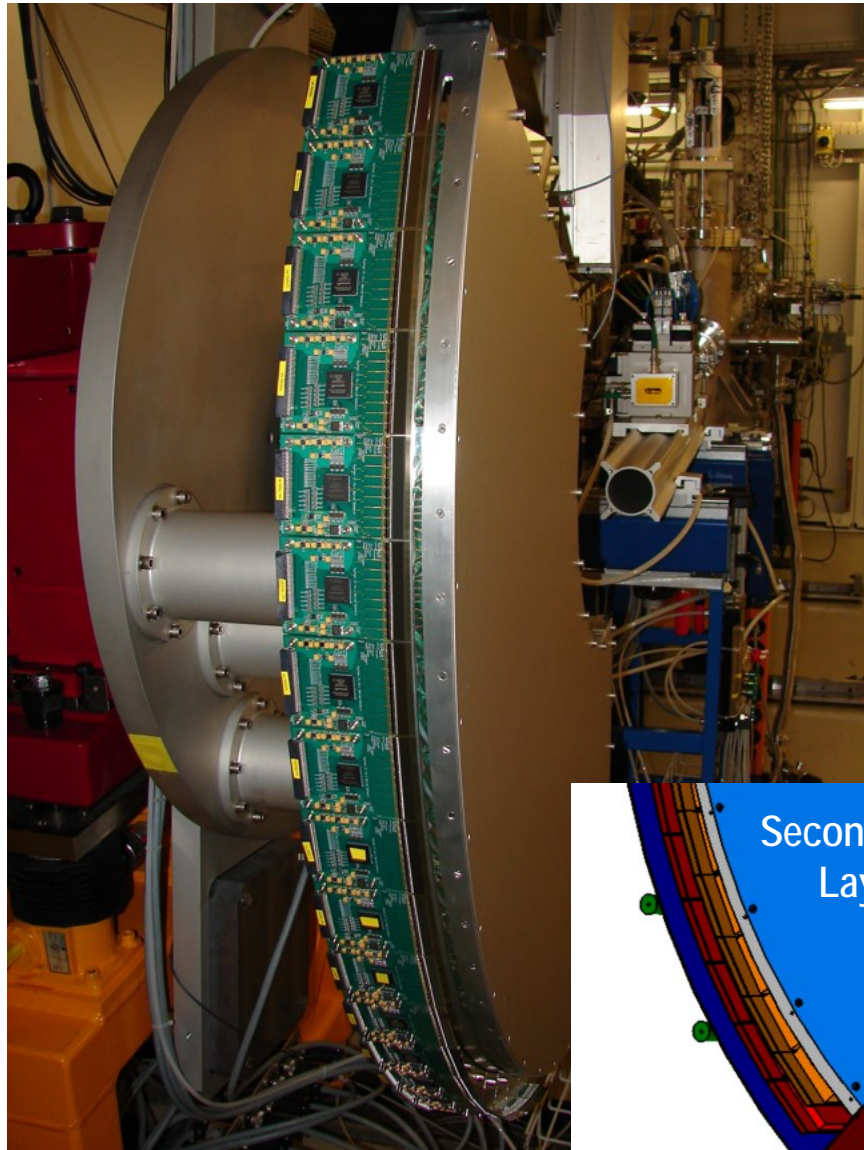


- Logic after comparator to:
  - Switch a 2<sup>nd</sup> time if 1<sup>st</sup> switch not enough
  - Avoid a 2<sup>nd</sup> switch on spikes due to the 1<sup>st</sup> one
- Switching has to be FAST (<10ns)

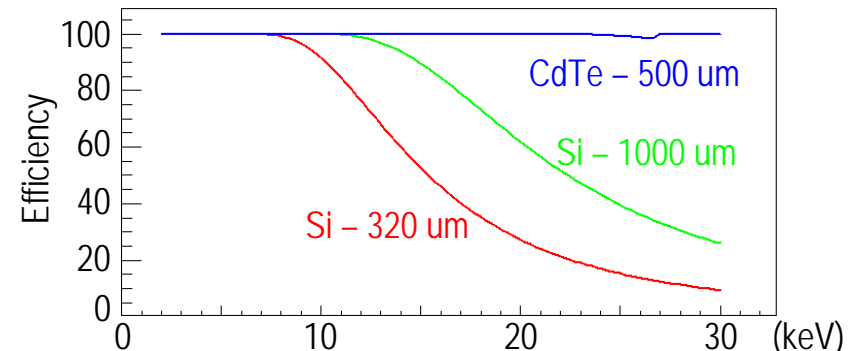
# Single photon counting detectors: Mythen, Eiger

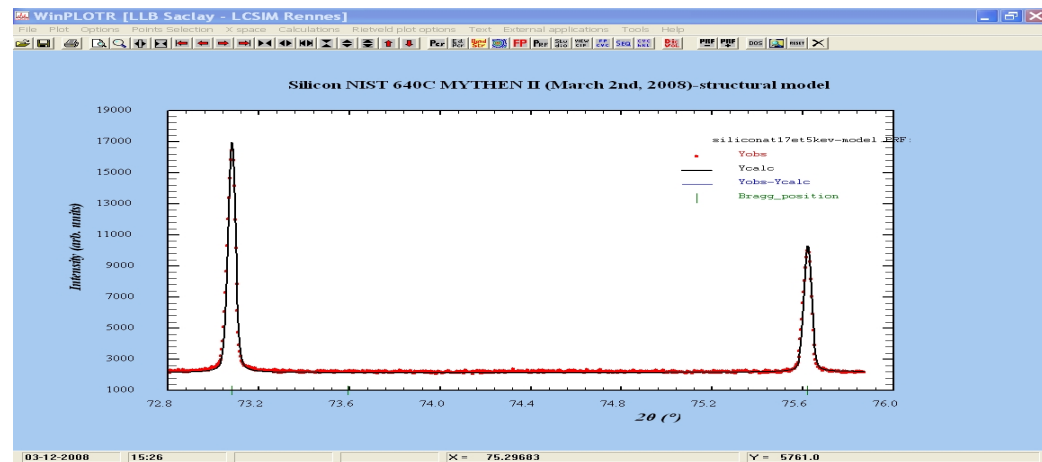
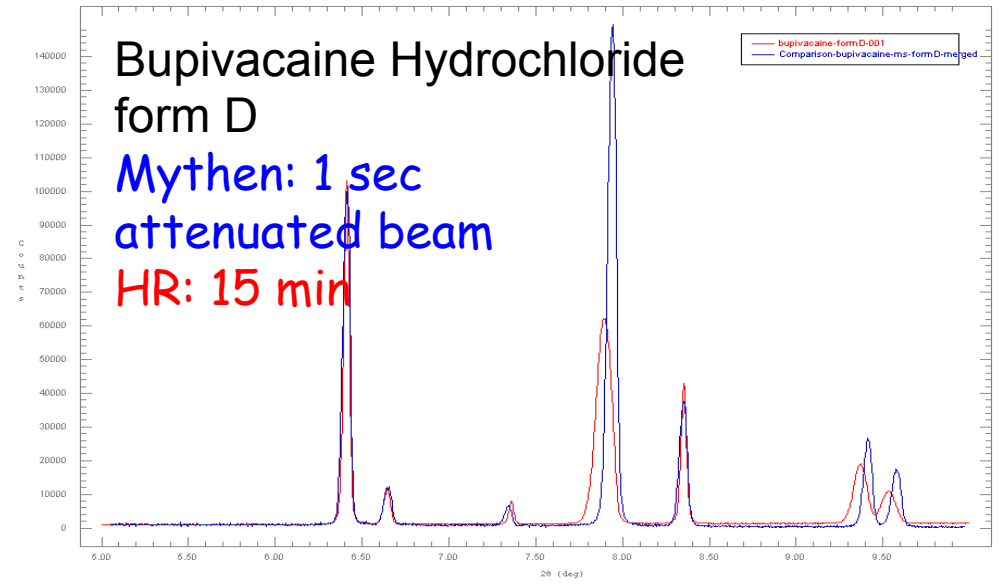
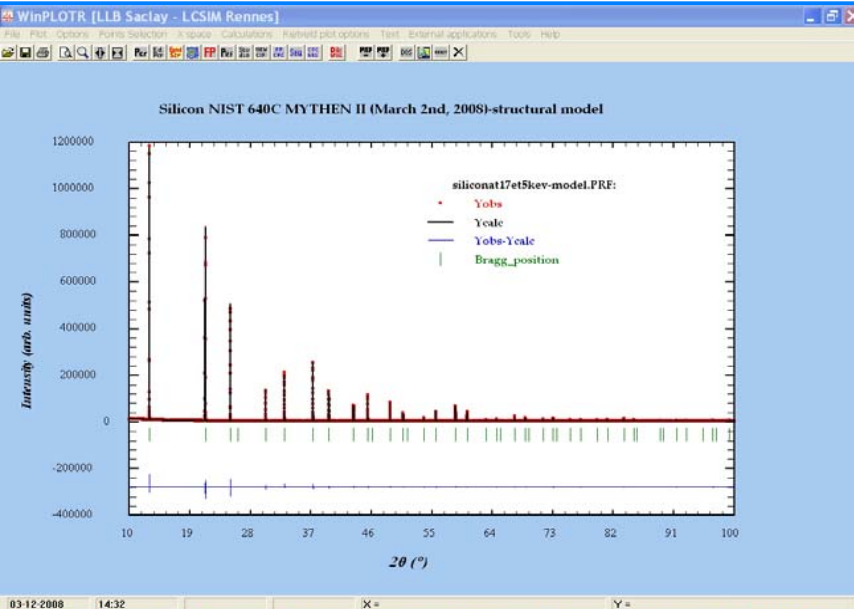
+1 +1 +1.....

# MythenII at the Powder Diffraction Station



- Modular system: 1 to 24 modules, for PD at SLS 24 modules covering 120°  
30k channels → gain factor of 30k
- Full 120° PD spectrum in <1s, typically 0.1s
- Intrinsic angular resolution 0.004° (0.01°)
- Energy range: 5-40 keV (at SLS PD)
- Main applications:
  - Time resolved, in Situ measurements
  - Temperature scans
  - Organic Materials (reduces radiation damage dramatically)

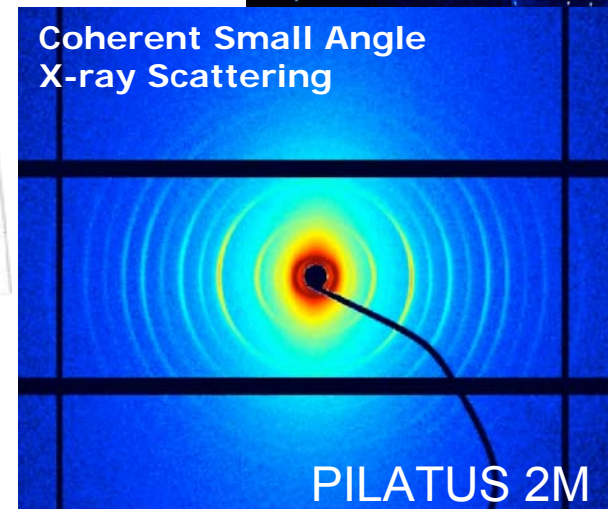
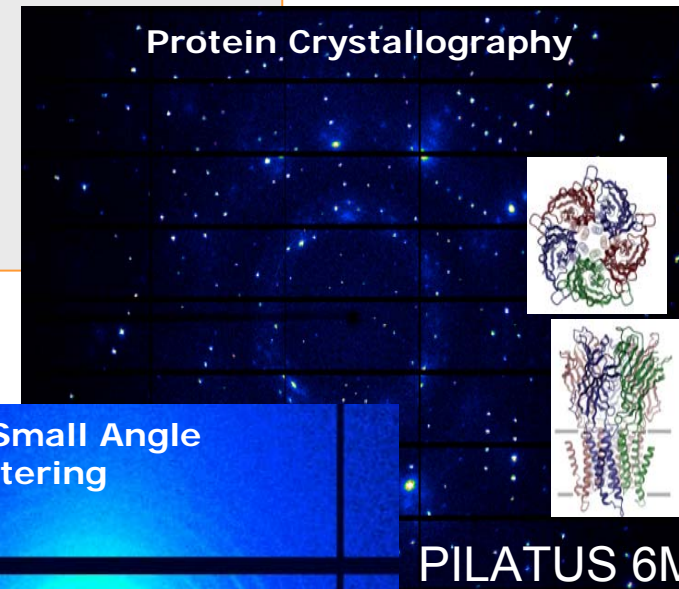
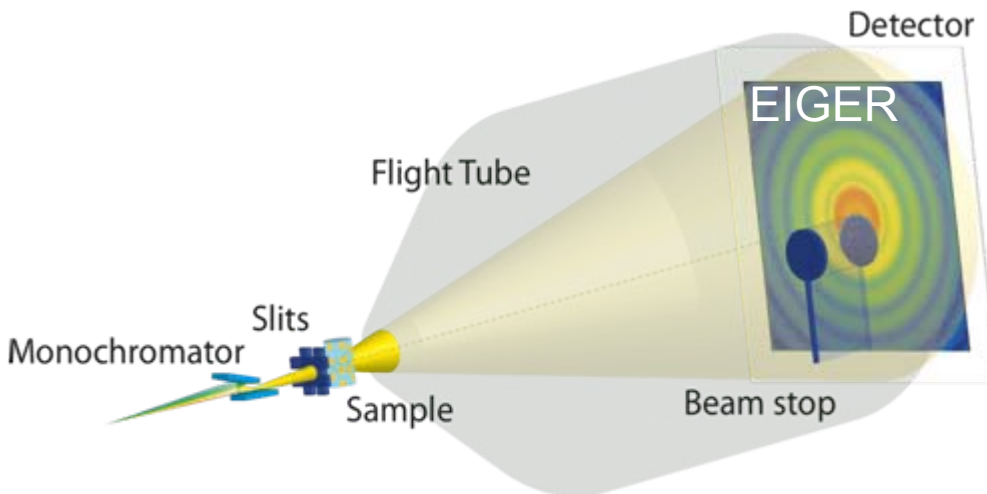




The data quality allows structural solution and refinement!  
Measurement several orders of magnitudes faster!

Single photon counting hybrid pixel detectors for synchrotron applications are aimed towards diffraction experiments

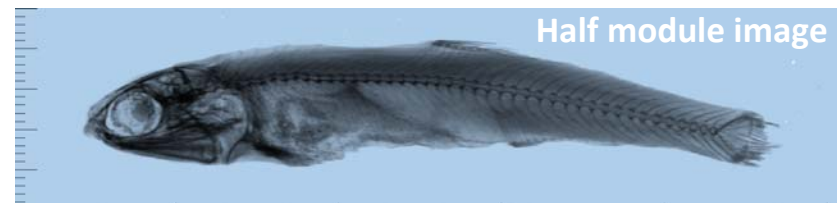
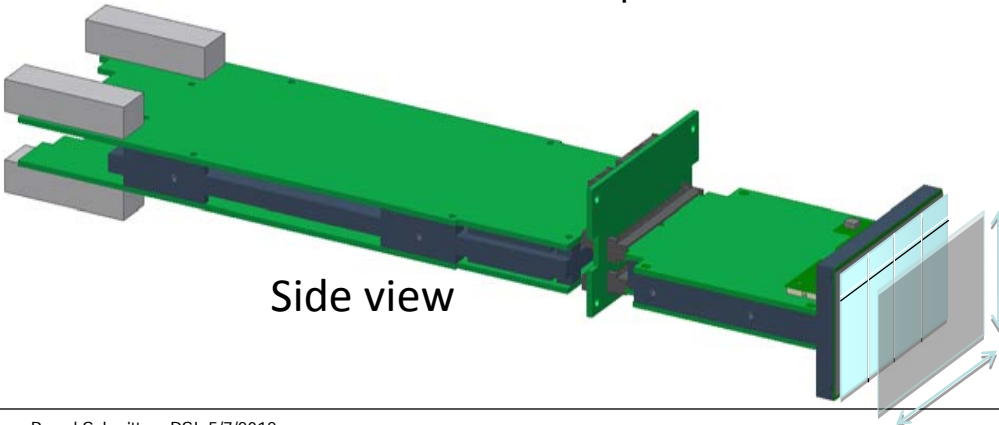
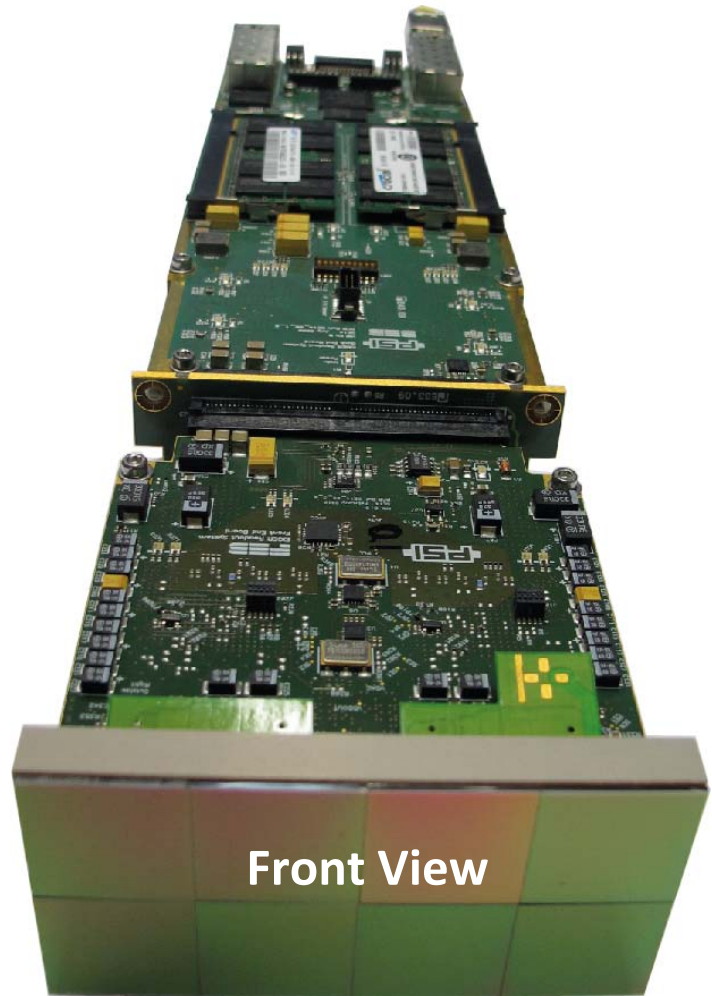
- Applications at CSAXs:
  - Scanning Coherent Small Angle X-ray Scattering
  - Coherent Diffractive Imaging
  - X-ray Photon Correlation Spectroscopy
- Protein Crystallography



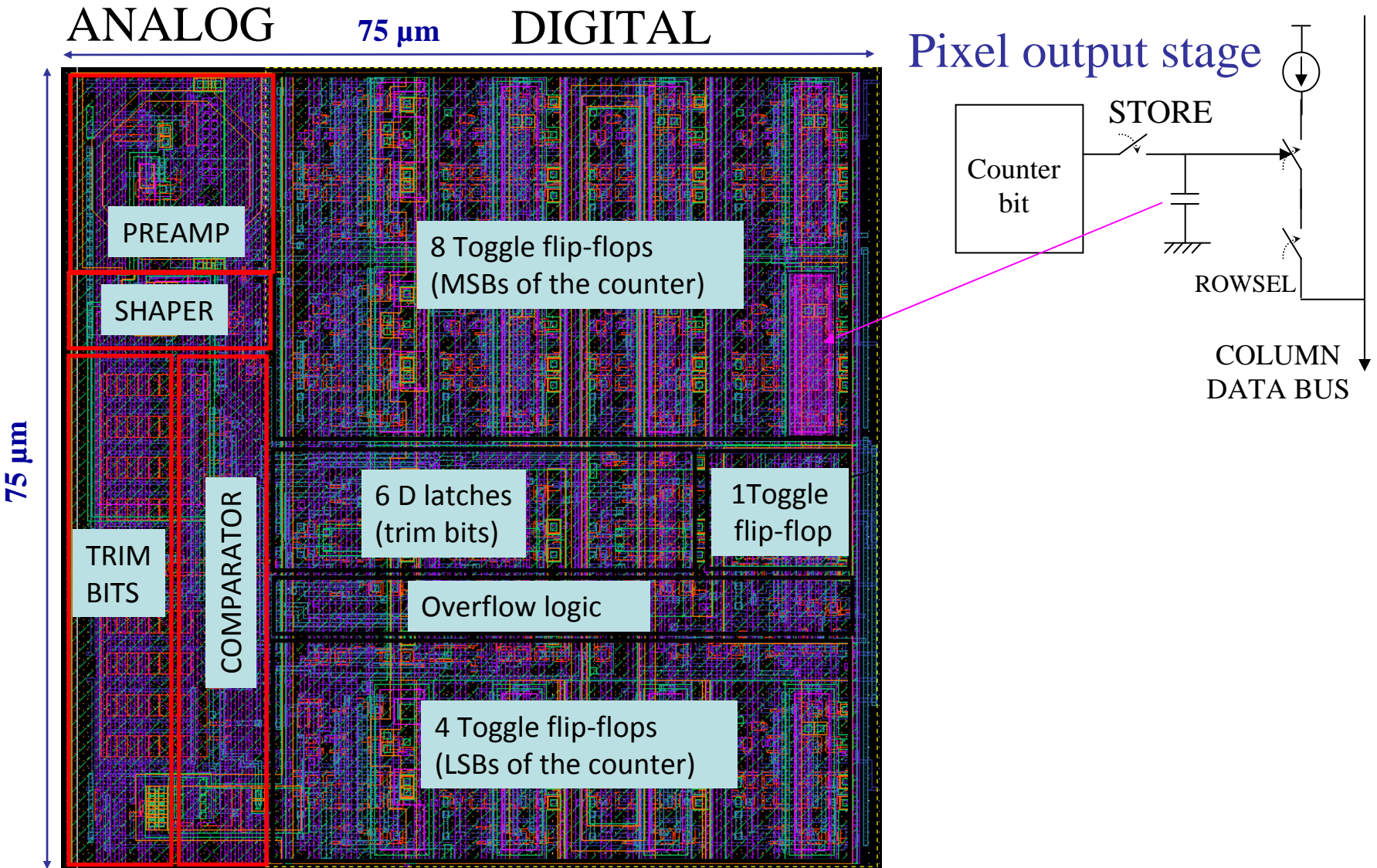
PILATUS 6M

# Eiger, the next generation pixel detector

- Single photon counting pixel detector
- Sensitive area of 38 X 77 mm<sup>2</sup>
- 524k pixel module
  - 2 x 4 chip array
- Dead time free mode of operation
- Maximum frame rates
  - 23 kHz in 4 bit mode
  - 12 kHz in 8 bit mode
  - 8 kHz in 12 bit mode
- Maximum data rates on the readout boards
  - 25 Gb/s for a half module
  - 50 Gb/s for a module
- On board in hardware data processing
- 8 GB of memory on a module
- Two 10 GbE data links per module



# The EIGER pixel on silicon



UMC 0.25  $\mu\text{m}$  Technology, full radiation tolerant layout

# EIGER main features

Technological process	UMC 0.25 $\mu\text{m}$
Radiation tolerance	Full radiation tolerant design (>4Mrad)
Chip size	19.3 x 20.1 mm <sup>2</sup> (active 19.2x19.2mm <sup>2</sup> ) > <b>2 x</b>
Pixel size	75 x 75 $\mu\text{m}^2$ = / <b>5.3</b>
Pixel per chip	256 x 256 = 65536 = <b>11.3 x</b>
Counter	12 bits, <b>binary, configurable (4,8,12 bit mode), double buffered</b>
Count rate	3.4 x 10 <sup>9</sup> x-rays/mm <sup>2</sup> /s = <b>5.3 x</b>
Dead time free readout	<b>yes</b>
Detector readout speed	~12 KHz @ 8 bit mode ( <b>Detector size doesn't matter</b> ) = up to <b>~2000 x</b> (Clock=100 MHz DDR)
Threshold adjustment	6 bit DAC

**In red:**  
Improvement factor with respect to PILATUS



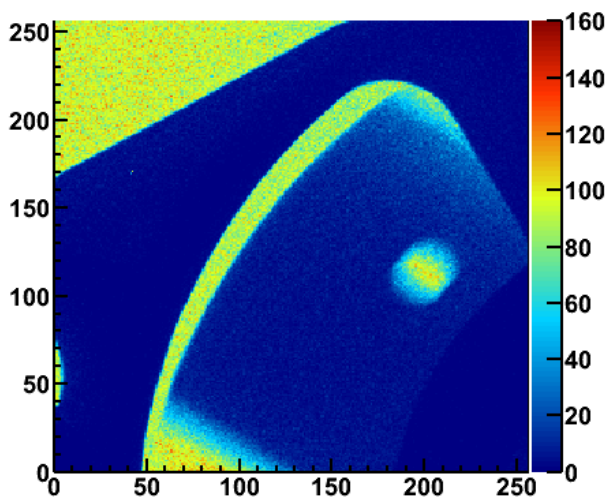
# EIGER high frame rate Demo

$V = 50\text{KV}, I = 0.4\text{mA}$

$V = 50\text{KV}, I = 1\text{ mA}$

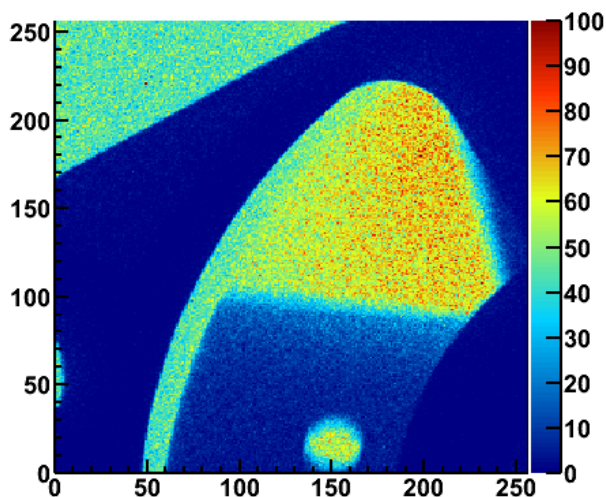
$V = 50\text{KV}, I = 1\text{ mA}$

Frame number 1



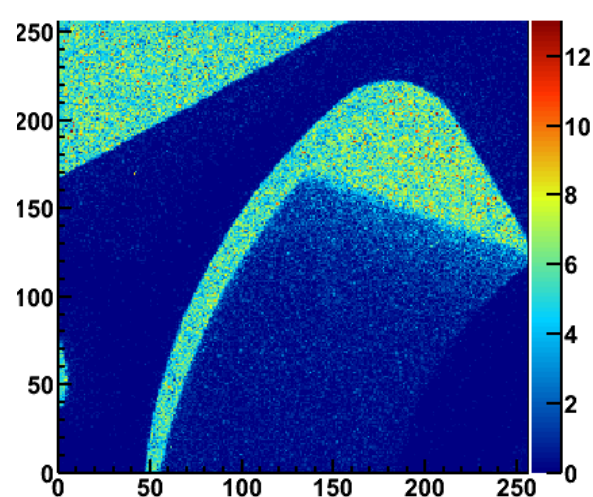
Chip in 12 bit Mode  
Exposure time  $125\mu\text{s}$   
Dead time  $3\mu\text{s}$   
Frame rate 7.8 kHz

Frame number 1



Chip in 8 bit Mode  
Exposure time  $85\mu\text{s}$   
Dead time  $3\mu\text{s}$   
Frame rate 11.4 kHz

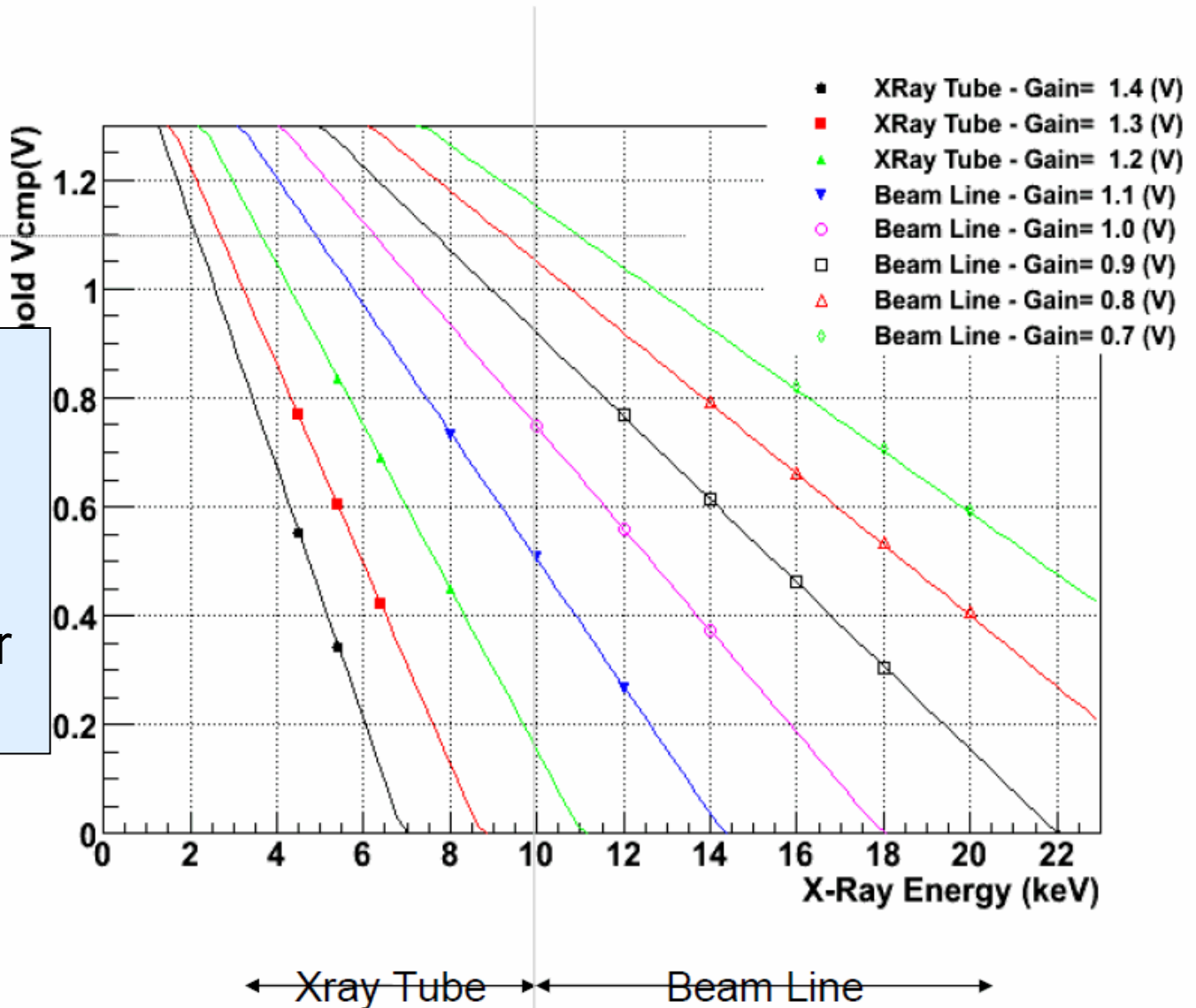
Frame number 1



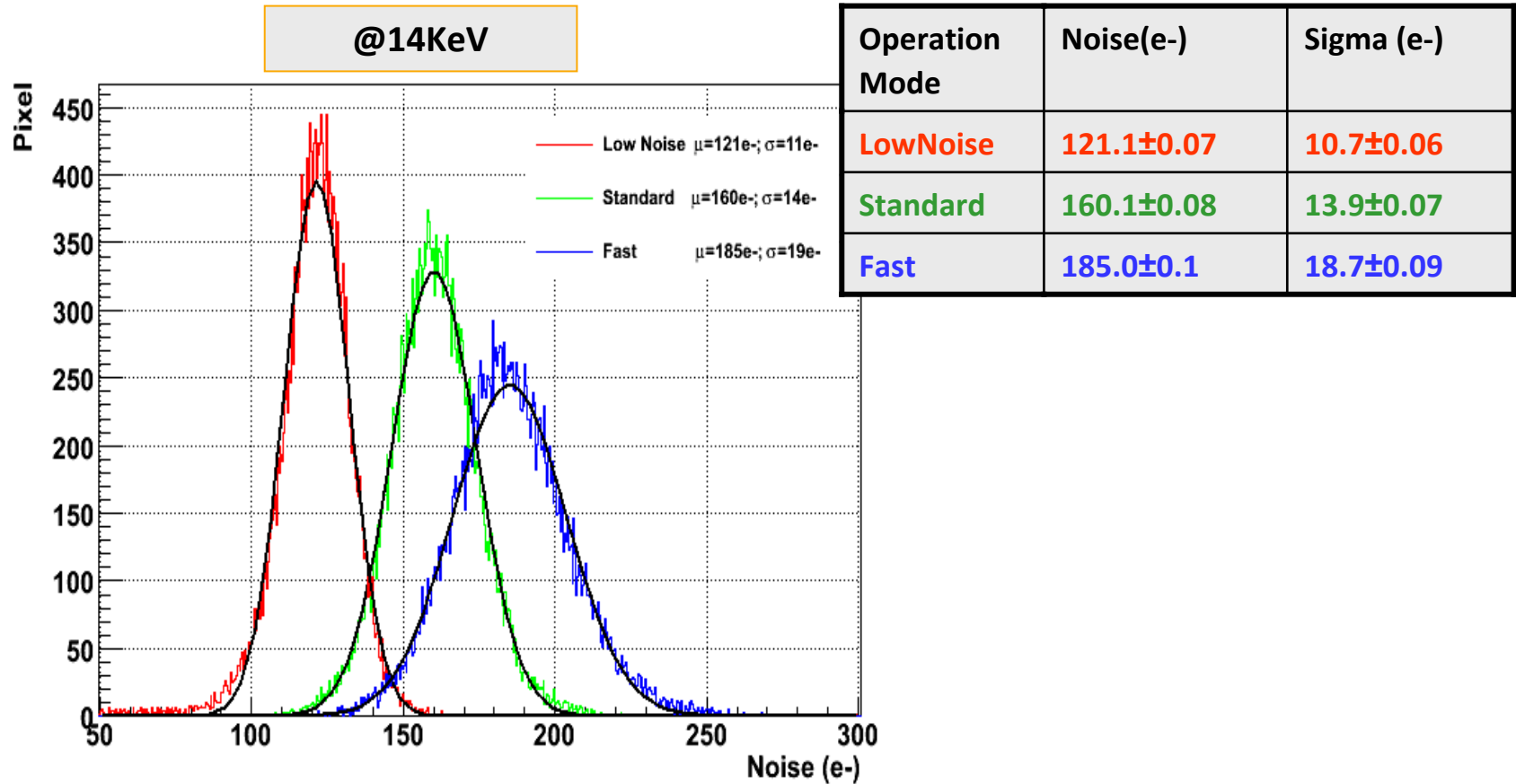
Chip in 4 bit Mode  
Exposure time  $45\mu\text{s}$   
Dead time  $3\mu\text{s}$   
Frame rate 20.8 kHz



„Gain“ controls the feedback transistor bias. Increasing gain = less noise, slower ret. Zero.

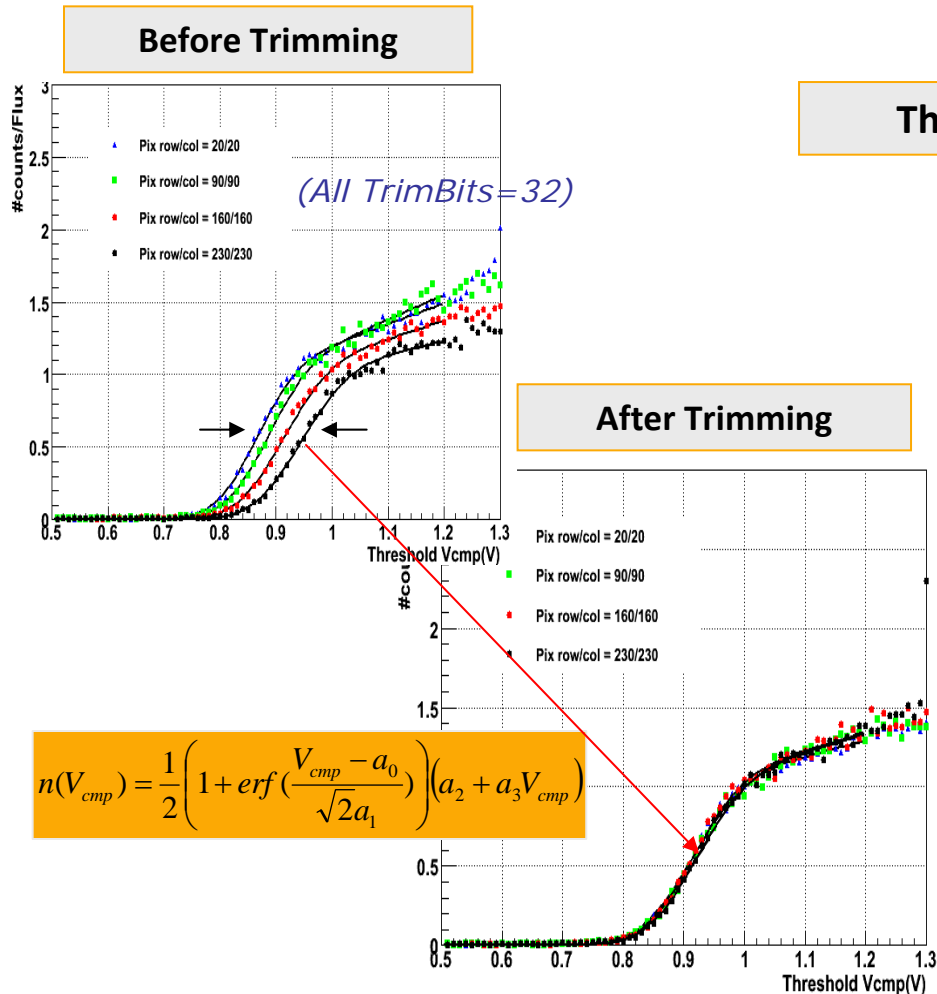


❑ **Noise distributions:** monochromatic beam at the Beam line



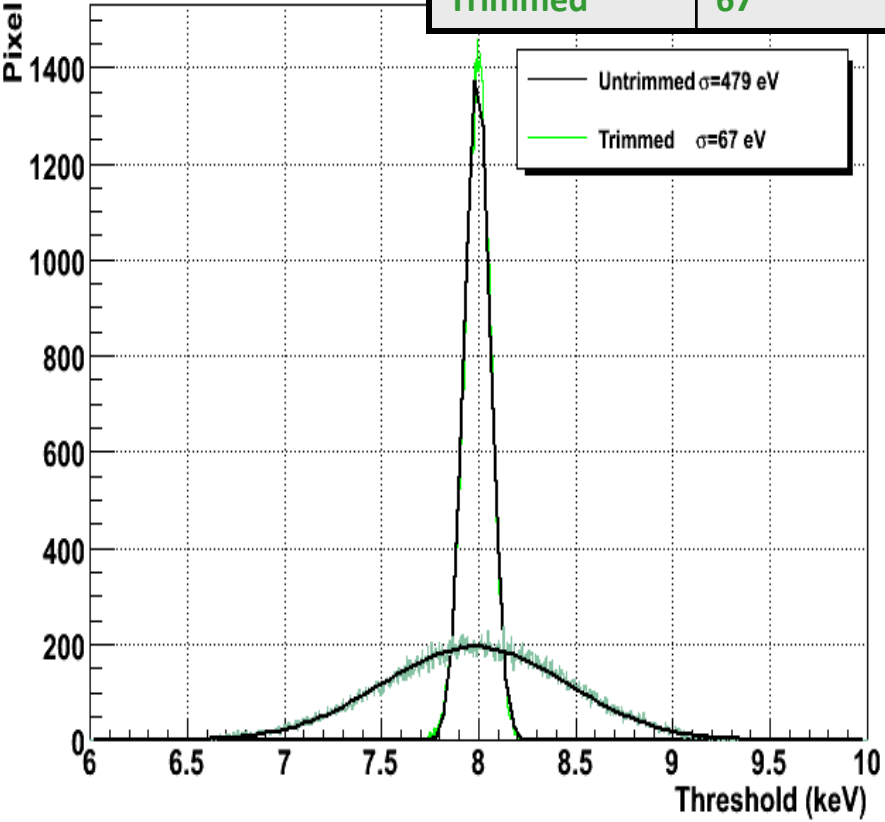
# Trimming

- Trimming: XRay tube and Fluorescence screens (Cu Screen 8KeV)
- Threshold scans: **Standard Mode** of operation



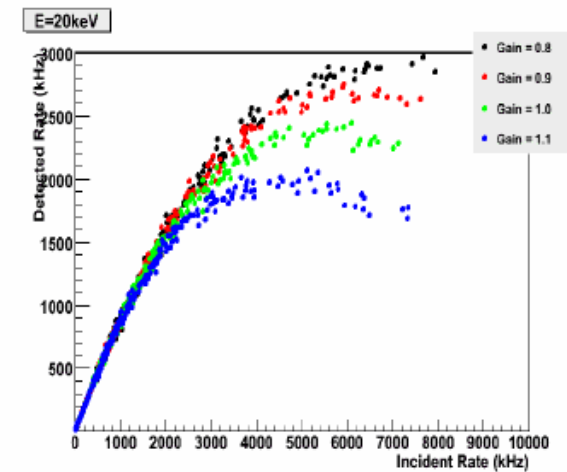
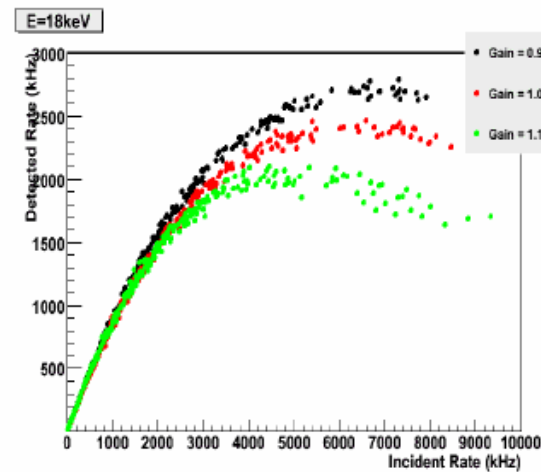
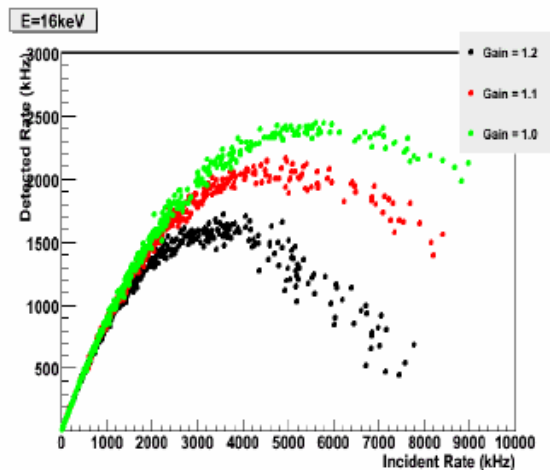
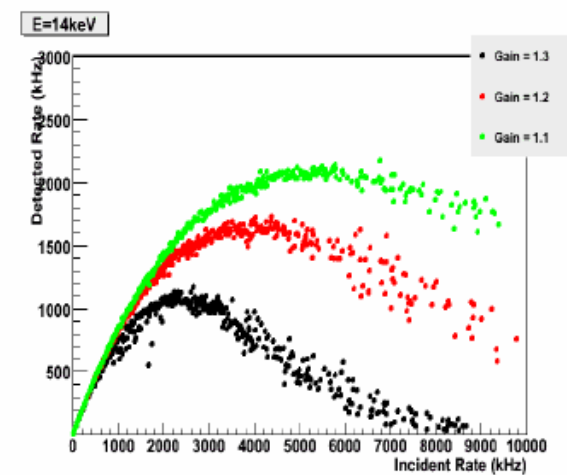
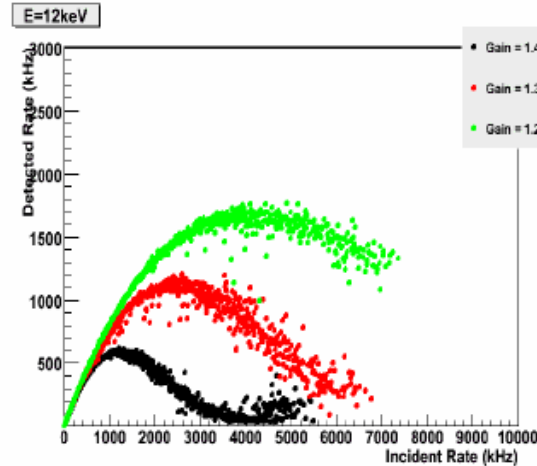
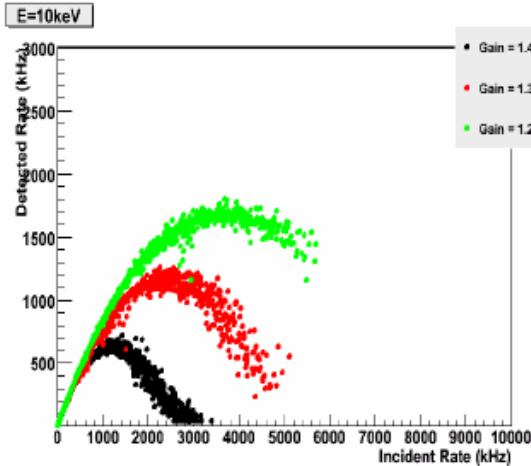
**Threshold dispersion**

	$\sigma$ (eV)
Untrimmed	479
Trimmed	67



---- Very first curves: no fit, no errors !

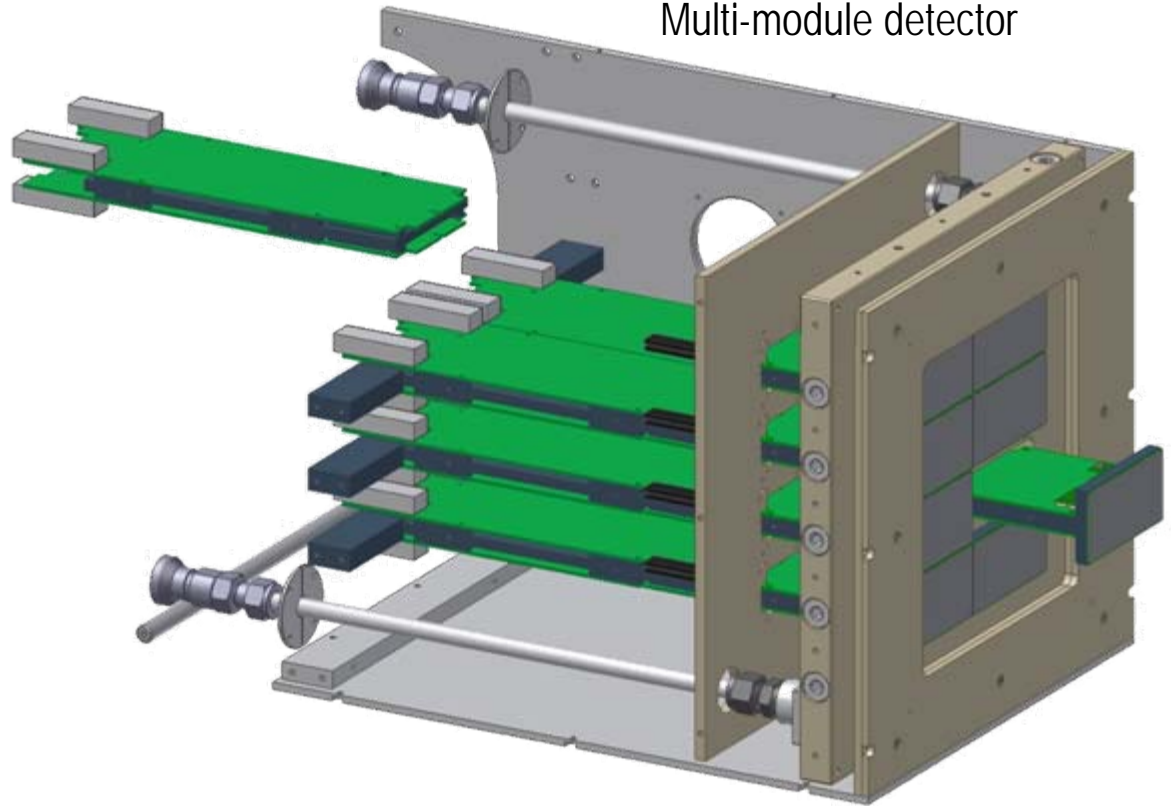
---- 3 gains per Energy (4 @ 20keV) Vcmp @ 1/2 E



500k single module



Multi-module detector



	Number of pixels	On board storage (frames/4 bits)	Data rate <sup>1</sup> @ 12 kHz	Data rate <sup>2</sup> @ 1kHz	Data rate <sup>3</sup> @ 100 Hz	Data rate <sup>4</sup> @ 10 Hz
Module	524 k (512 x 1024)	~32,740	50.3 Gb/s	6.29 Gb/s*	839 Mb/s*	168 Mb/s*
9M Detector	9.44 M (3072x3072)	~32,740	906 Gb/s	113 Gb/s	15.1 Gb/s*	3.02 Gb/s*

1) 8 bit, equivalent to ~4@23 kHz and 12@8 kHz. 2) 12 bit. 3) 16 bit. 4) 32 bit. \*) Foreseeable continuous storage rates (~20 Gb/s).

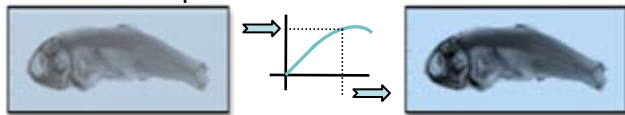
## Data buffering



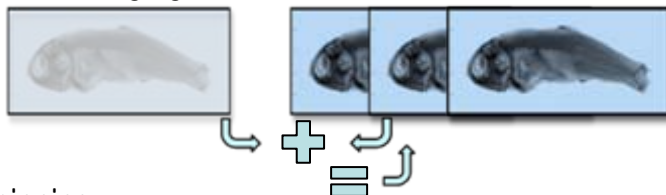
## Image summation



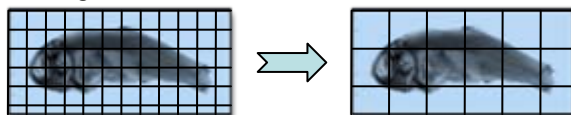
## Rate correction



## Series averaging



## Rebinning



- On board data processing is in parallel on multi-module systems
  - independent of the detector size
  - reduces a modules tens of Gb/s at the source
  - or a 9Ms, hundreds of Gb/s at the source
- Data buffering
  - on board memory for 32 k frames per 4 counter bits
- Image summation
  - extends the dynamic range from 4096 to  $4 \times 10^9$
  - ~1 k sub image frame rate (4 M counts/pixel/s)
  - transparent to the user
  - makes high flux continuous data taking possible
  - reduces the quantity of data at the source
- Rate correction
  - performed on sub-frames @ kHz frame rate
  - more precise, less sensitive to rate fluctuation
  - real-time processing
- Pump and probe series averaging
  - high frame rate exposure series summing
  - alternating pumped and un-pumped
  - no data transfer dead time between series
  - huge reduction of the quantity of data at the source
- Data reduction
  - 2x2 pixel rebinning
  - SAXS ring intensity averaging (planned)
  - data compression (in thought, question of HDF5 compatibility)

# Charge integrating systems

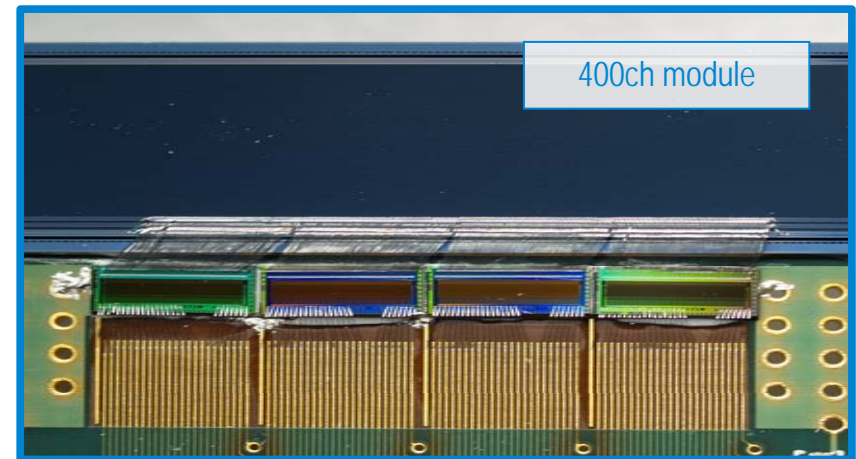
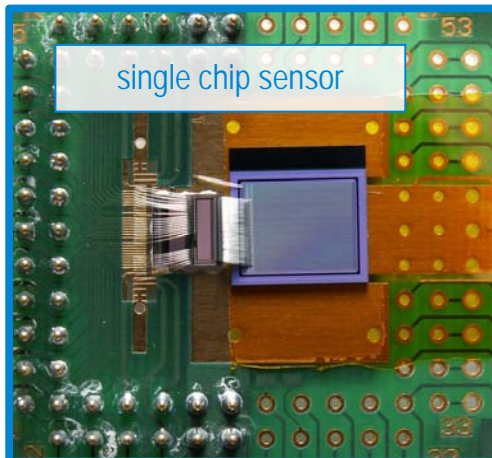
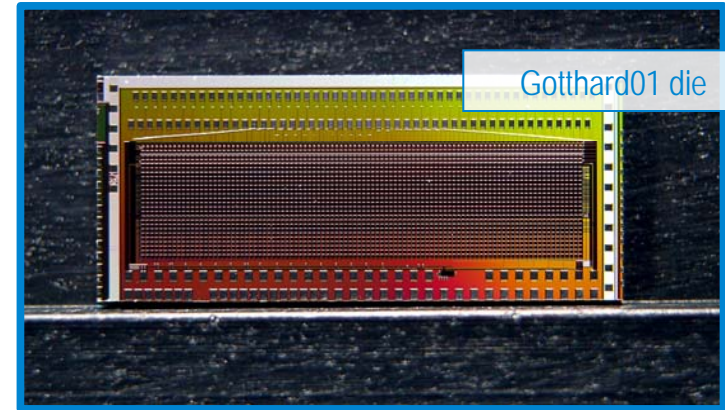
**+1 +1000 +100.....**



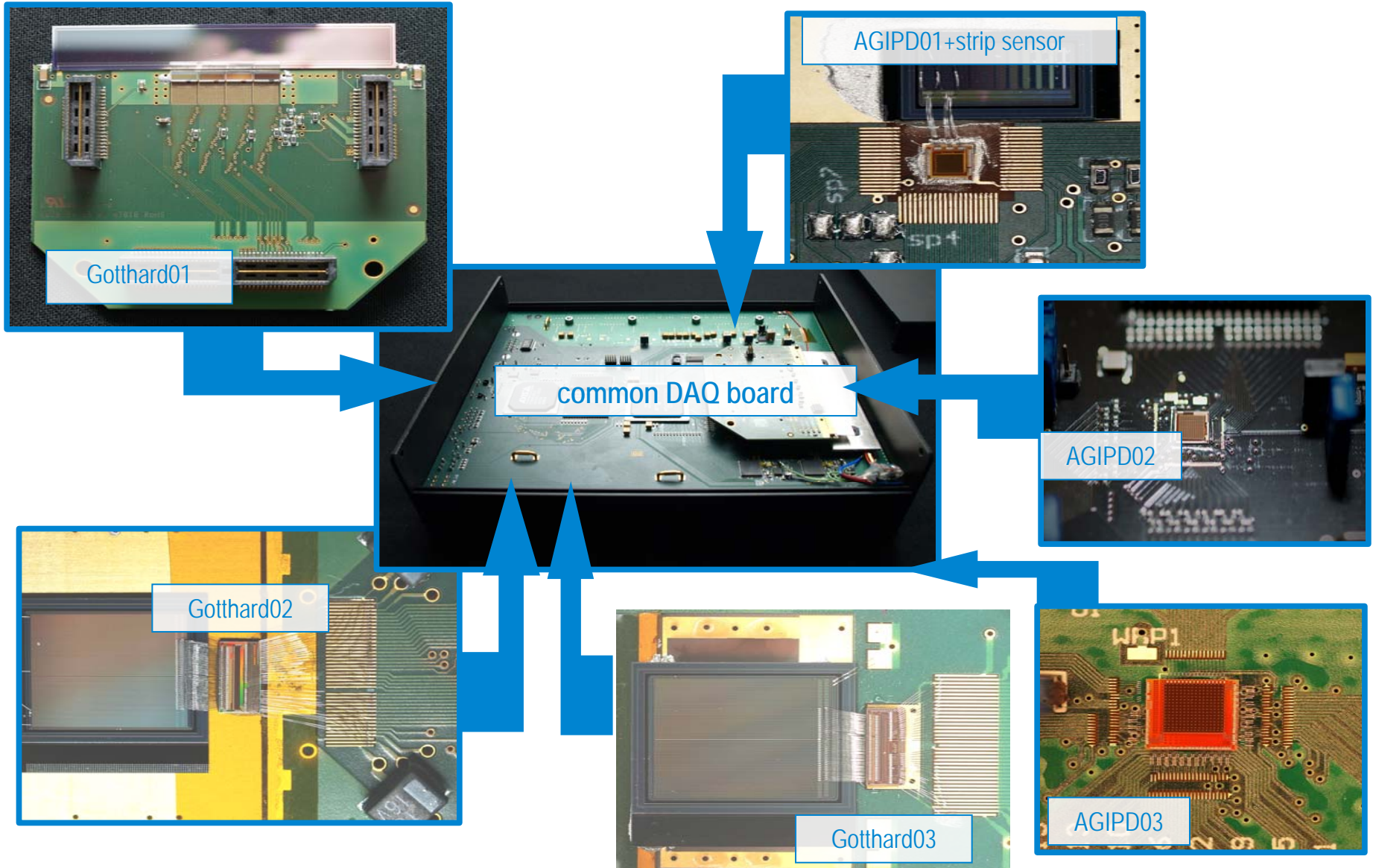
## GOTTHARD:

Gain Optimizing microstrip system with Analog Readout

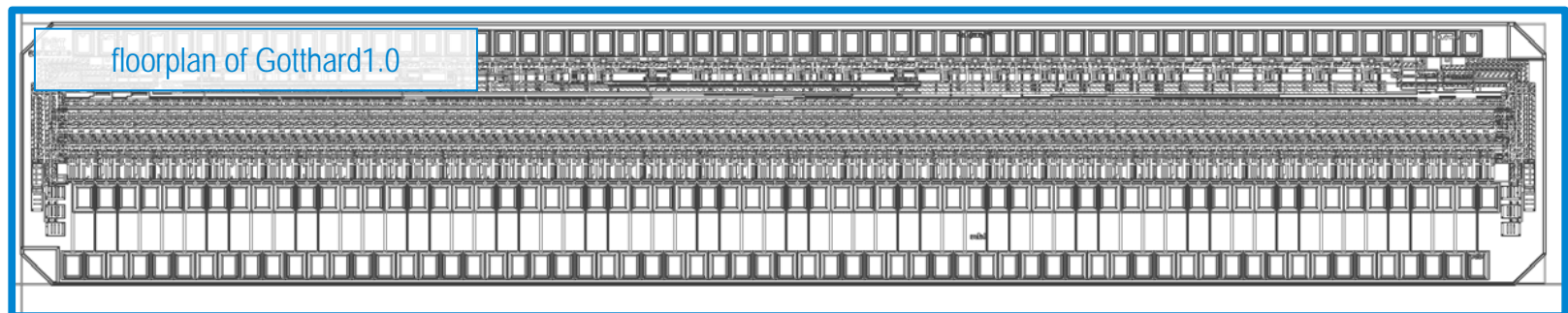
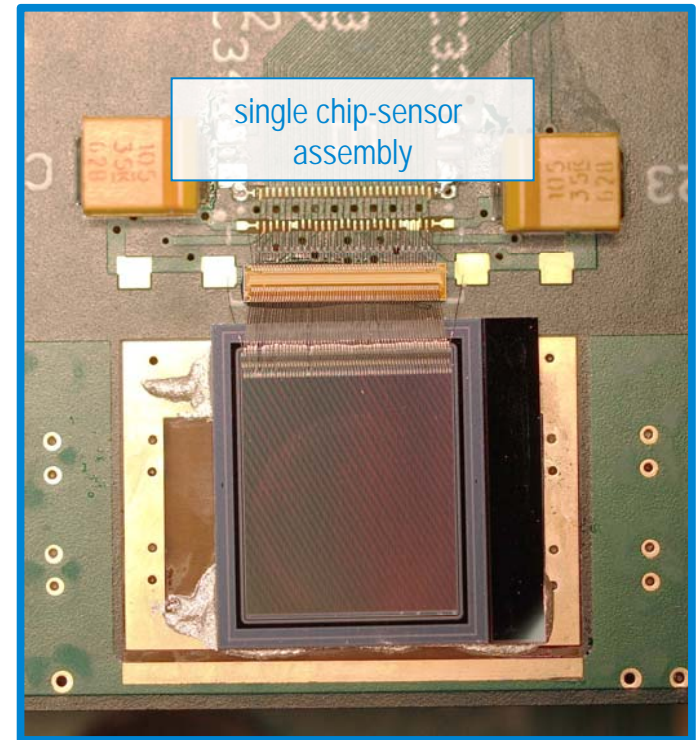
- designed in 2004 in UMC0.25 $\mu$ m technology
- 100 channels
- 4 gain stages with automatic gain switching
- double sample and hold circuit to perform offline CDS



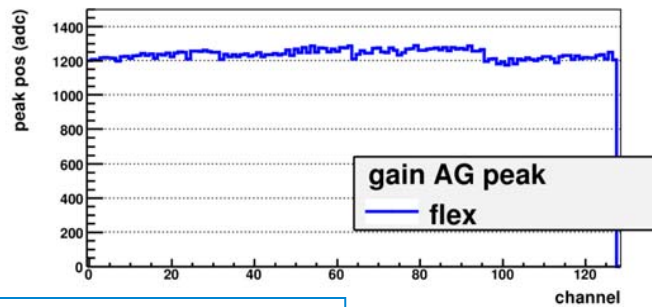
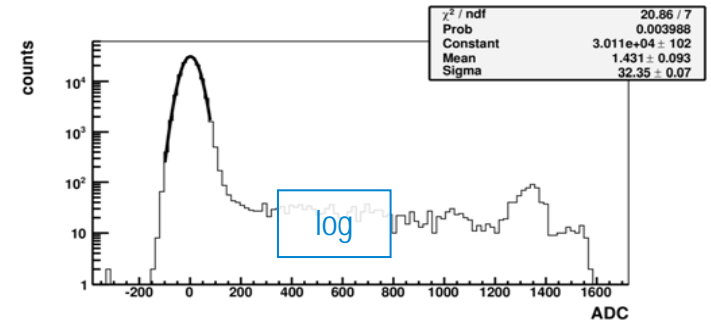
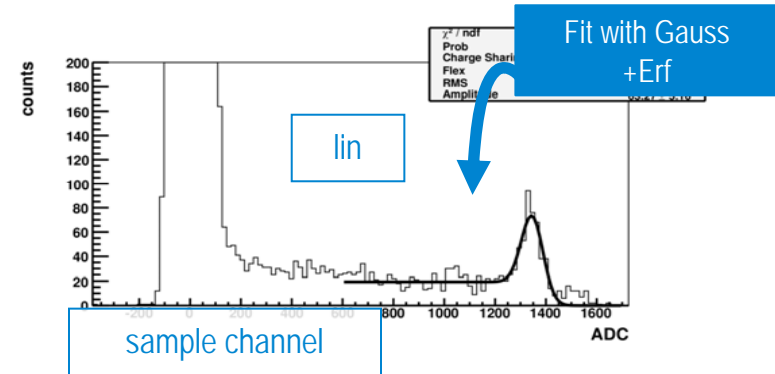
# Prototypes (2008-2010)



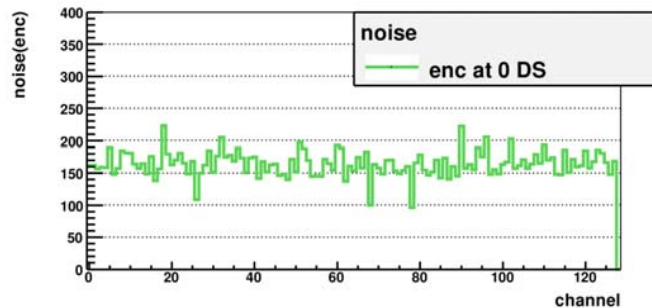
- $6.3 \times 1.4 \text{ mm}^2$  - 128 channels -  $50 \mu\text{m}$  pitch
- 3 automatic gain stages + 1 High Gain mode
- fast off pixel buffers, to sustain 32MHz readout
- 4 diff. analog outputs, 8 digital (gain) outputs
- $\sim 1 \text{ mW/ch.}$



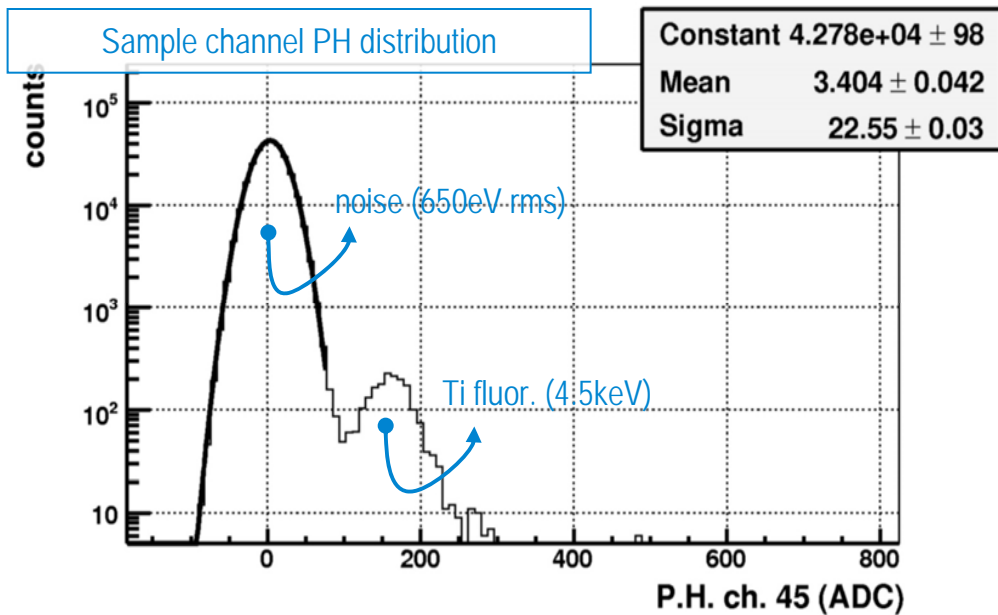
- measured with X-ray tube and Ag fluorescence (22 keV)
- ~ 1 us integration time



ch. to ch. variations

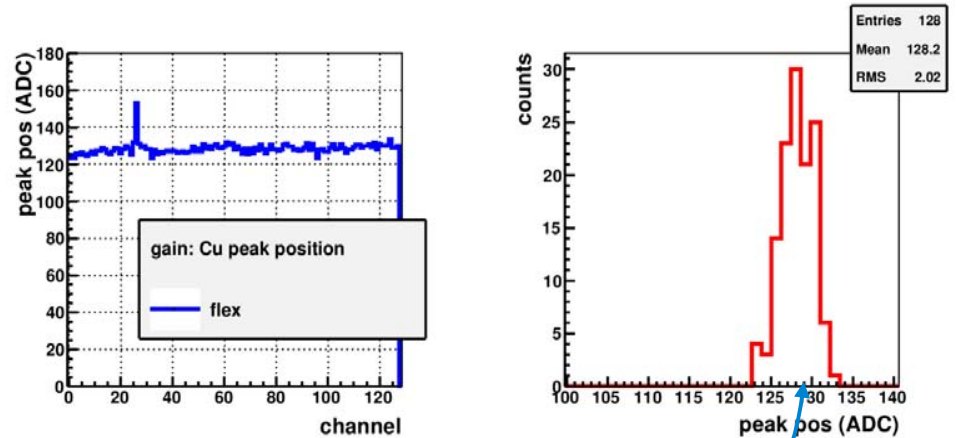


Noise ~ 160 e.n.c.

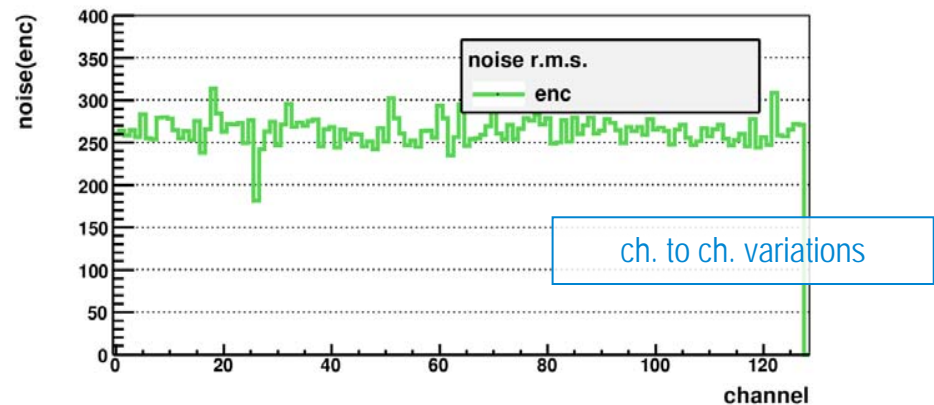
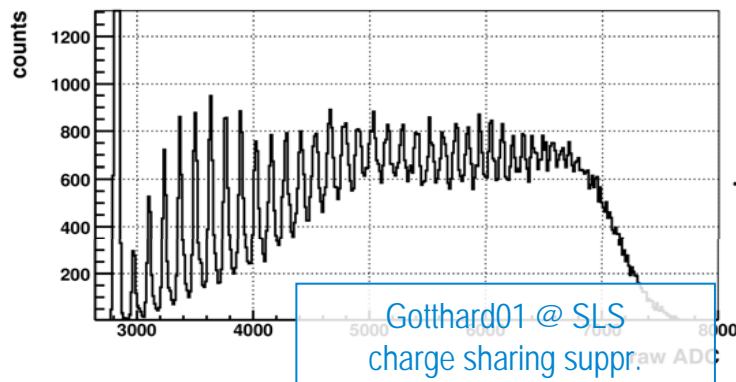


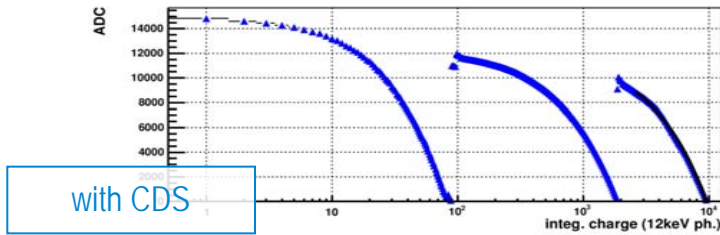
- single photon resolution down to  $\sim 3.5$  keV
- At lower energy 2 or more  $\gamma$ s can still be separated from the noise floor
- Sensors with thin “entrance window” needed

- Gain1 is the starting gain for the switching mode
- measured with Cu fluorescence (8 keV)
- Average noise is 260 e.n.c.
- SNR=13 (for a 12 keV photon signal)
- Gain1 total range ~ 80 ph.

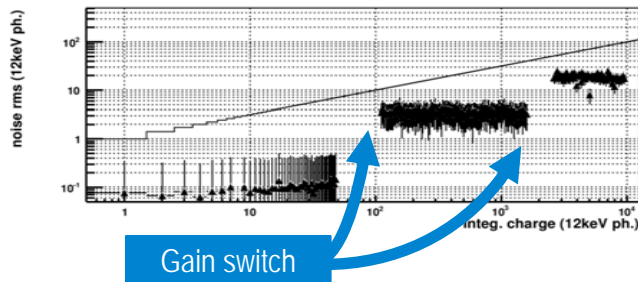


gain dispersion 1.5%

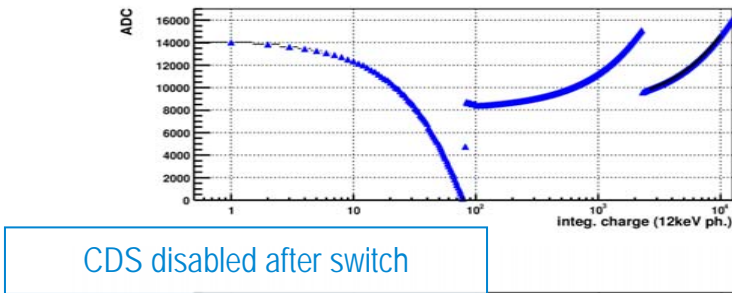




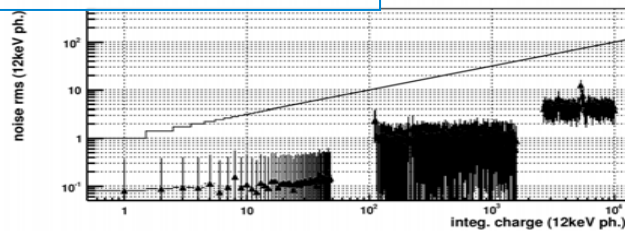
- an integration time scan at constant input current (visible light) was used to evaluate the noise at low gains



- CDS increases the noise of gain 2 and 3: a circuit to disable CDS after switch is present

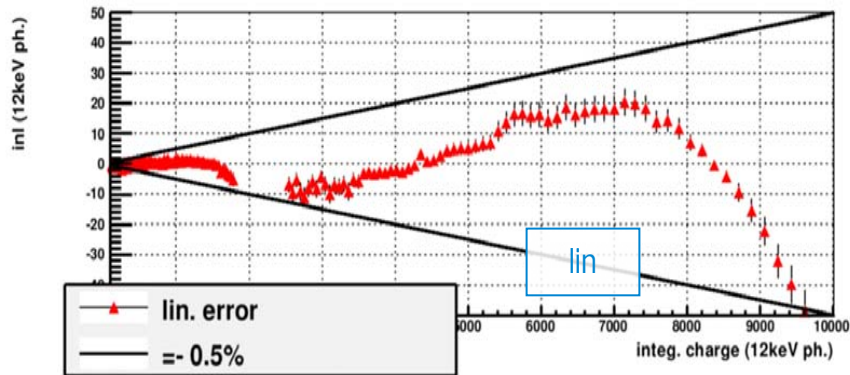


- at all gains the electronic noise is well below the Poisson level



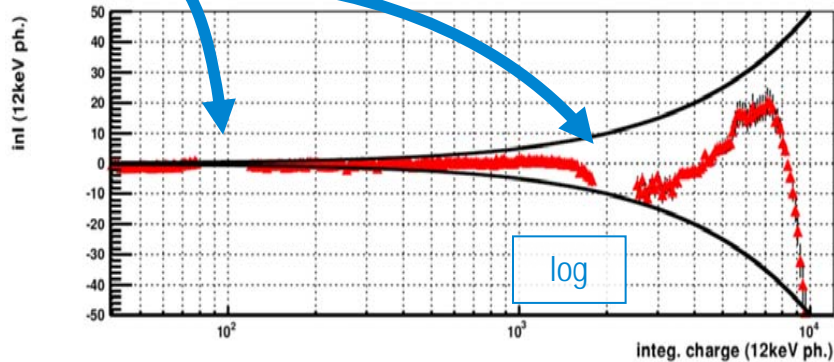
- Dynamic range of  $10^4$  ph.

CDS disabled after switch



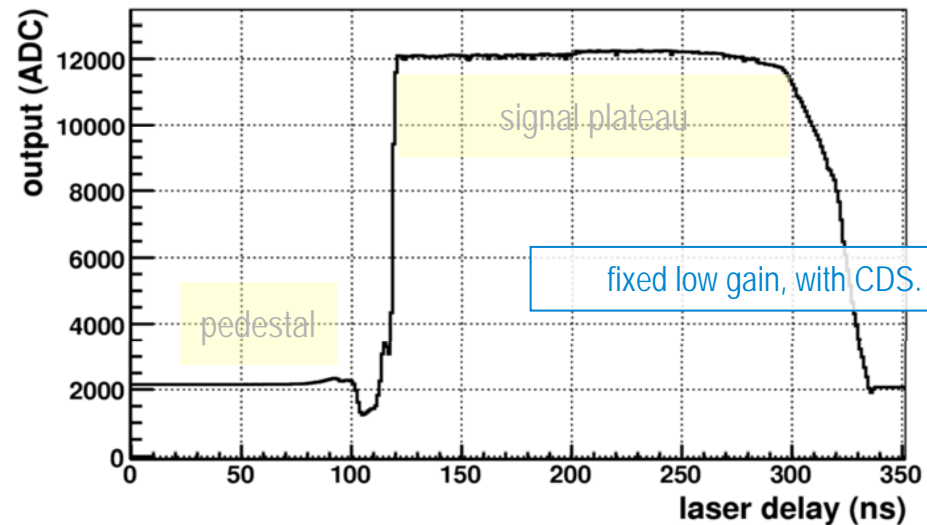
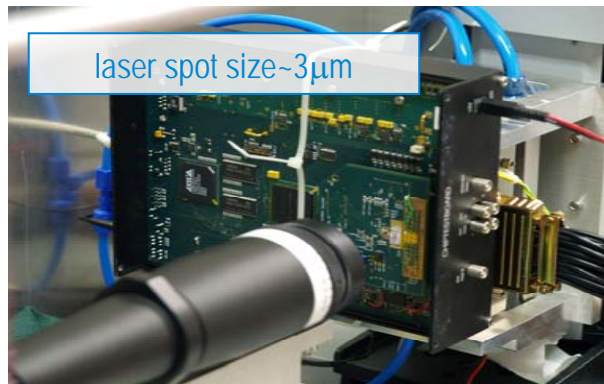
- linearity errors within  $\pm 0.5\%$  (source effects included) in the design input range (0- $10^4$  ph.)
- On smaller ranges better linearity can be achieved

Gain switch



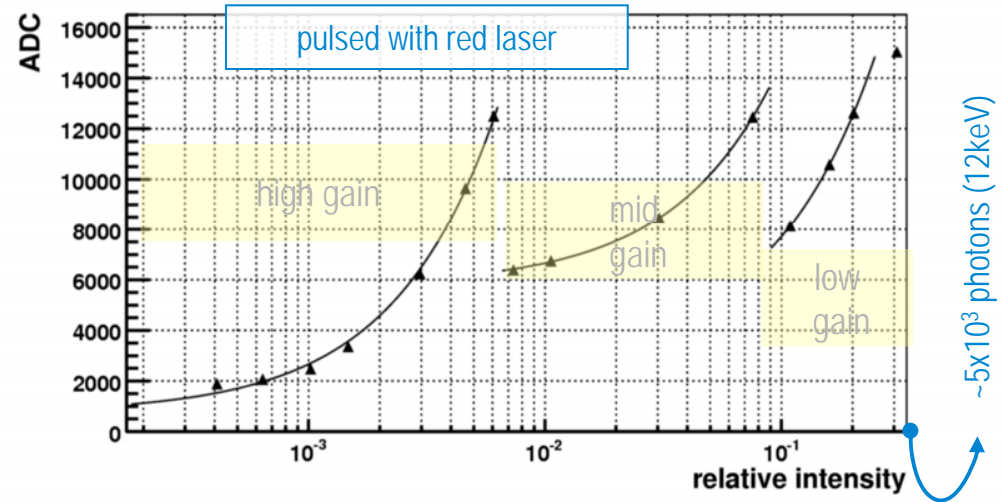
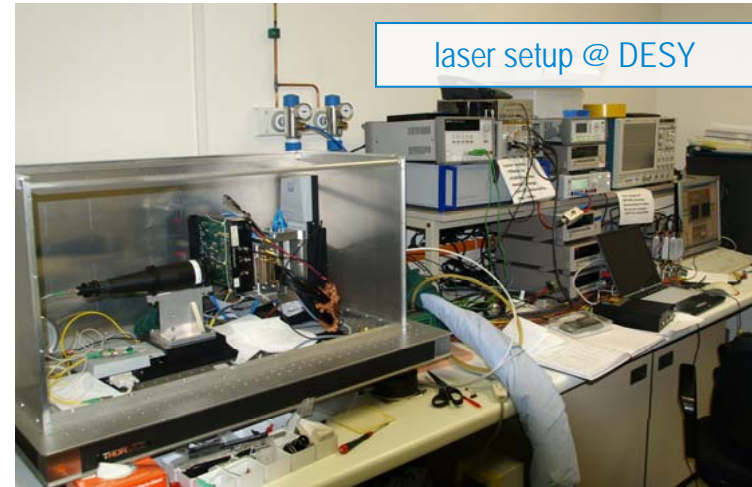


- ps laser setup UNI HH
- delay scan with a high intensity laser pulse (~500 12keV photons)
- 200ns integration time
- CDS output settles in <30ns
- integration times as small as 80-50ns can be used

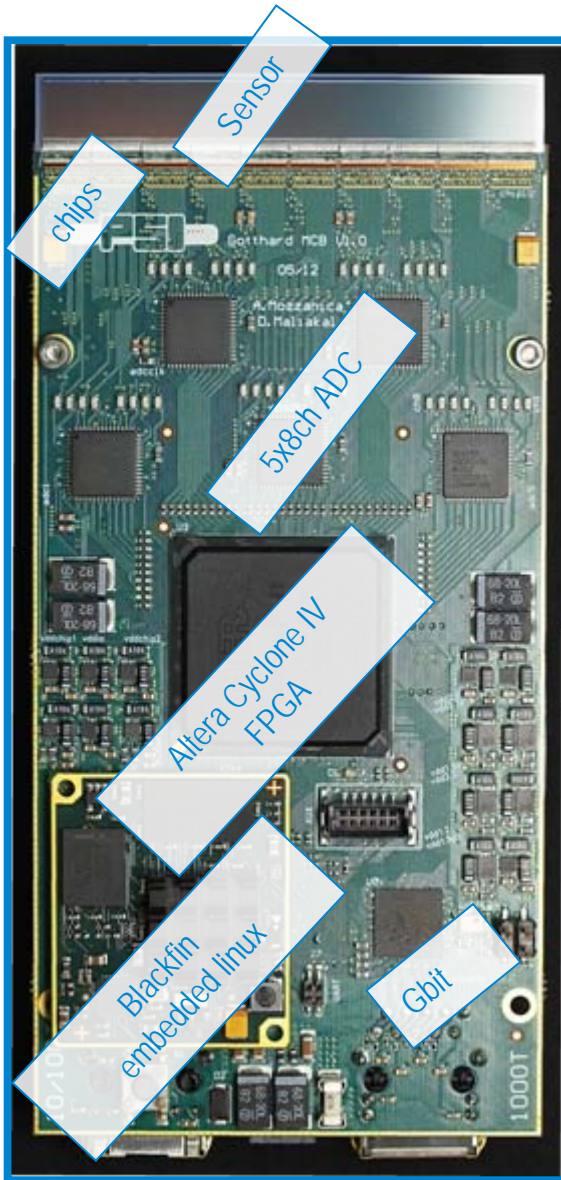


- similar result (in terms of speed) with or without automatic gain switching
- the preamplifier+switching circuitry can work at E-XFEL rates (4.5MHz)

- Measured with Gotthard0.3 prototype
- The switching has been tested with a sub-nanosecond laser pulse hitting the strip sensor
- Integration time 200ns, pulse in the middle of it
- Point dispersion mainly due to the uncertainty on the laser attenuation filters
- Switching works at the required speed



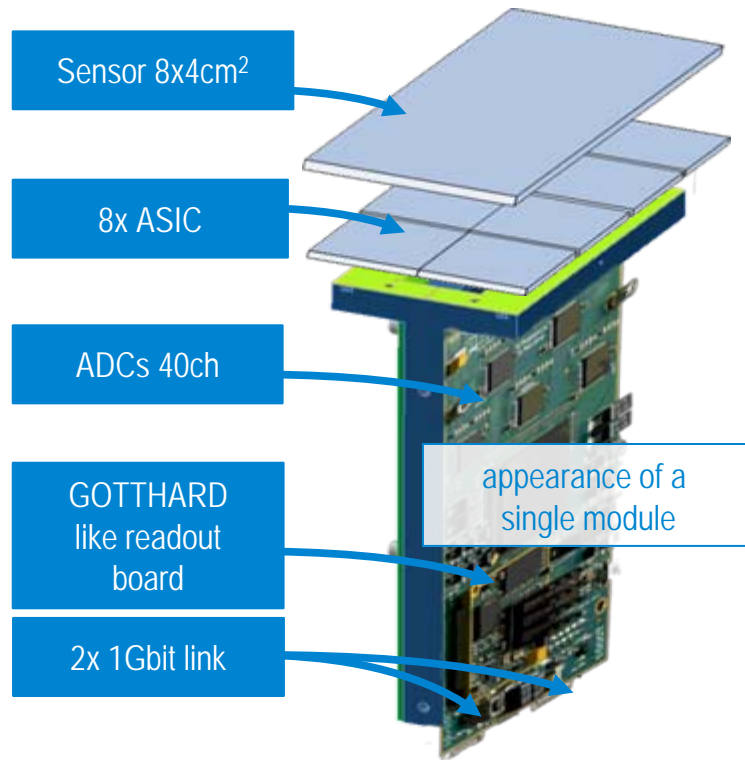
# GOTTHARD module: overview



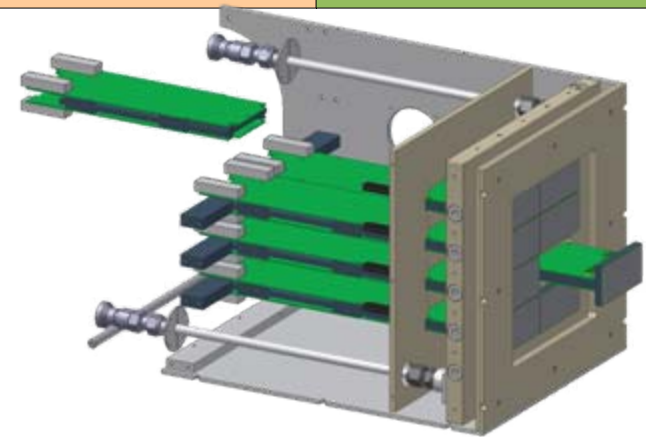
- 67mm x 130mm
- 50  $\mu\text{m}$  pitch, 1280ch/module (same as MYTHEN)
- 10 chips, 4 analog outputs per chip
- 40 ADC channels @50Mhz, 14bits
- Gbit Ethernet data transfer for readout
- 100T Ethernet for slow control/setup
- Fast readout (1MHz) with ~600 bunches per E-XFEL train measurable (memory for ~350)
- 60kHz continuous frame rate
- Integration in Mythen software (detector class)

	Specifications
module size	6.7x13 cm
sensitive area	64x10mm
sensor thickness	320-500 $\mu\text{m}$
pitch	50 $\mu\text{m}$
dynamic range	$10^4$ 12keV photons
min Energy	<3.5 keV
linearity	better than 0.5%
point spread function	O(pitch)
min int. time	80ns
dead time	<50ns
cooling	air (fan)
readout time = 1 / frame rate	>50kHz continuous 1MHz burst
XFEL ready	YES

## adJUstiNg Gain detector FoR the Aramis User station



ASIC technology	UMC110nm
module pixel count	525k
module size	80x40 mm <sup>2</sup>
sensor thickness	320-500 μm
pixel size	75x75 μm <sup>2</sup>
dynamic range	up to 10 <sup>4</sup> 12keV photons
noise r.m.s.	<150 e.n.c.
min Energy	<3 keV
linearity	better than 1%
point spread function	1 pixel
dead time	<50ns
cooling	liquid
readout time = 1 / frame rate	400Hz



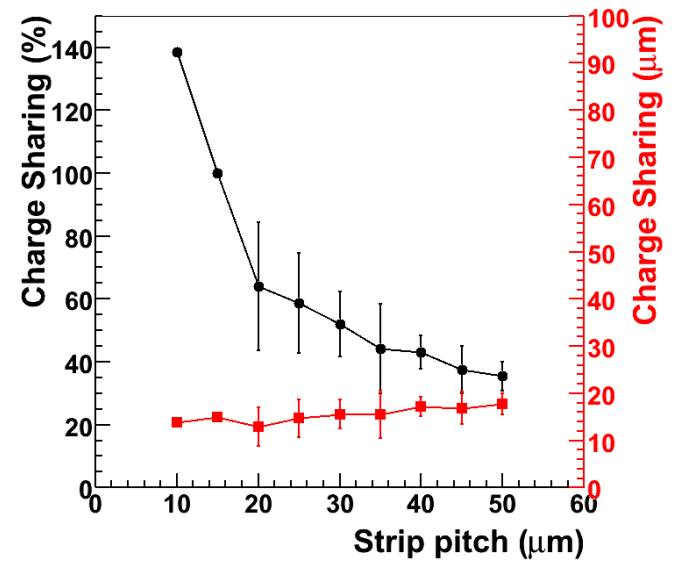
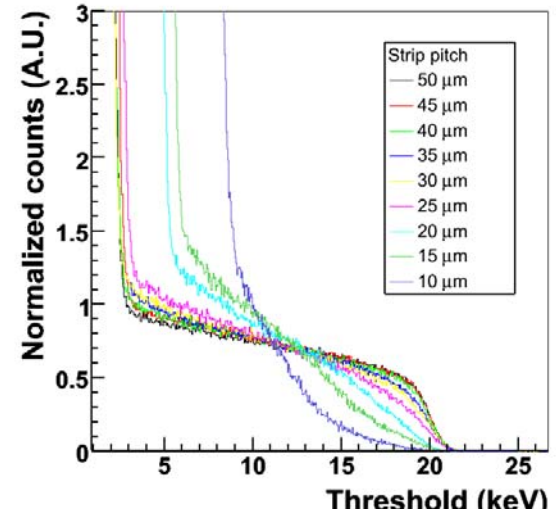
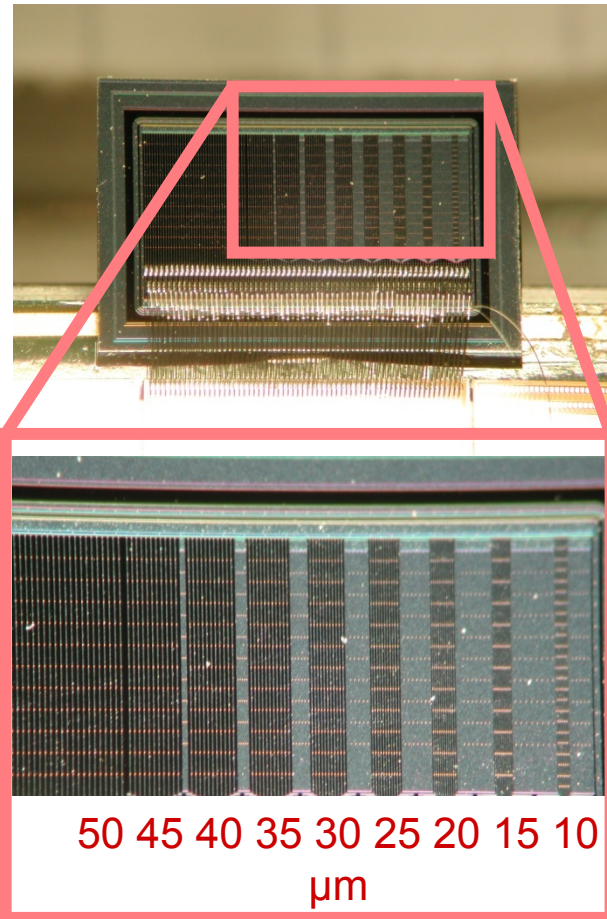
EIGER 4M detector  
mechanics – 4x2 modules

- ASIC and readout system based on GOTTHARD
- Dimensions, sensor and mechanics from EIGER
- chip 2013, module 2014, detector mid 2015

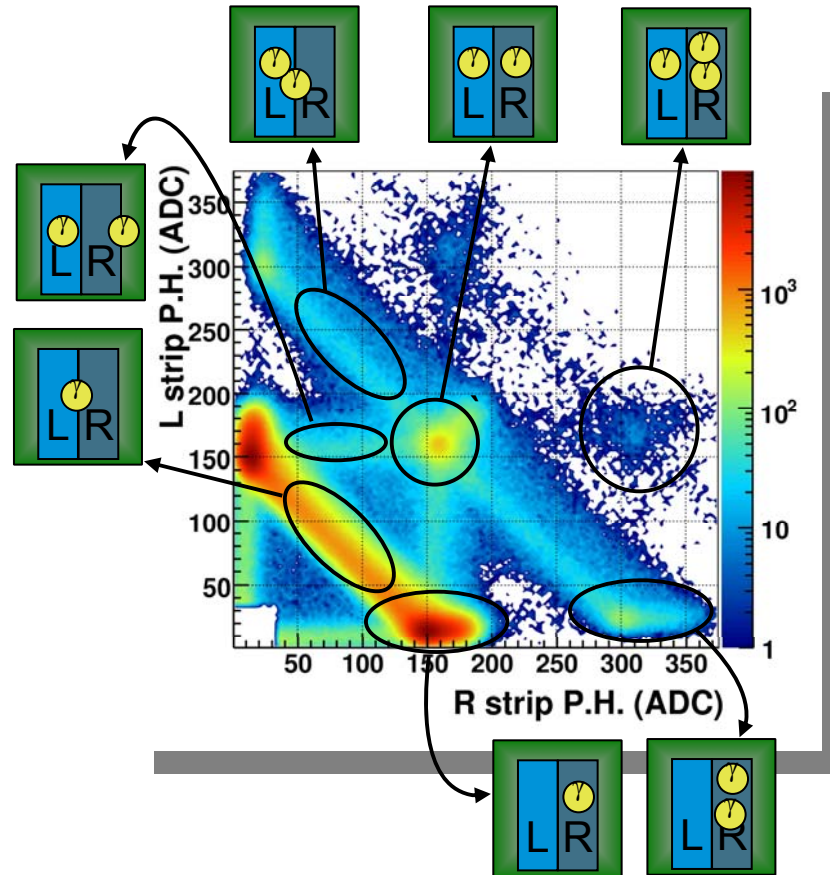
What are the limits for the pixel size and position resolution?

How can one achieve the highest resolution?

# Single photon counting detectors at small pitches



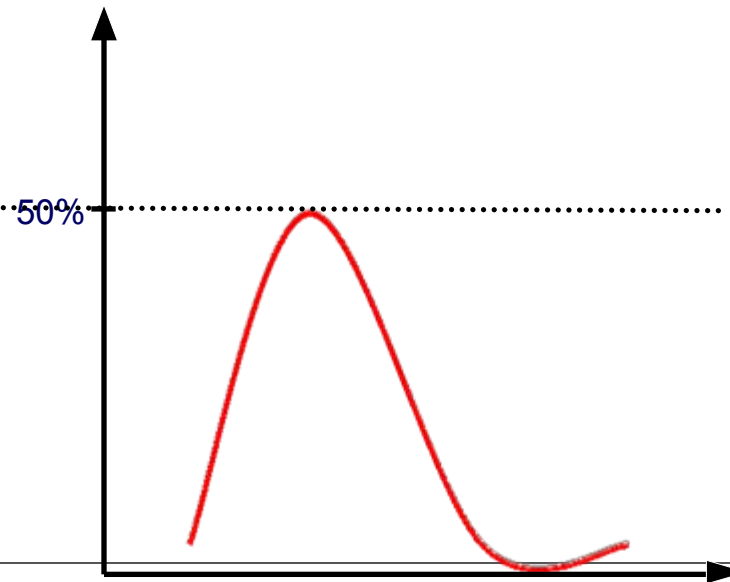
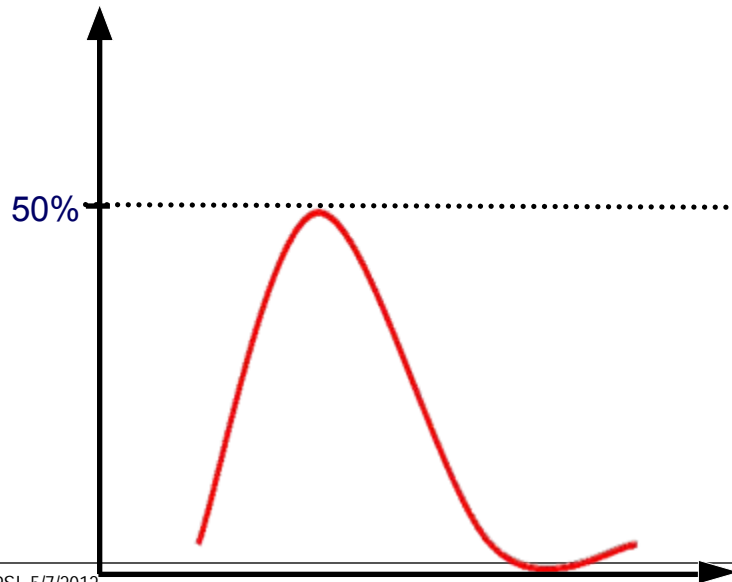
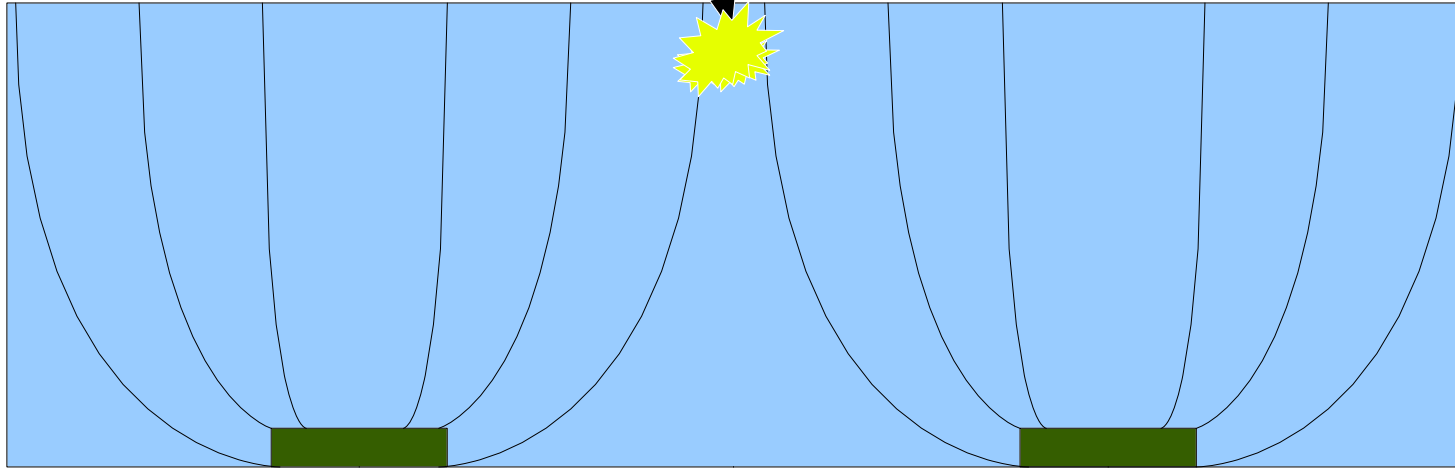
single photon counting possible down to about 25 μm  
 below 25 μm charge summation necessary  
 the region where the charge is shared is approximately constant



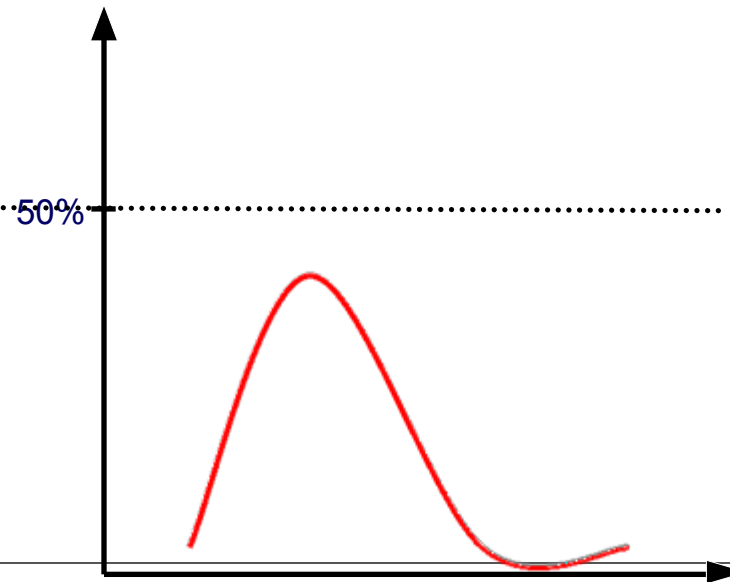
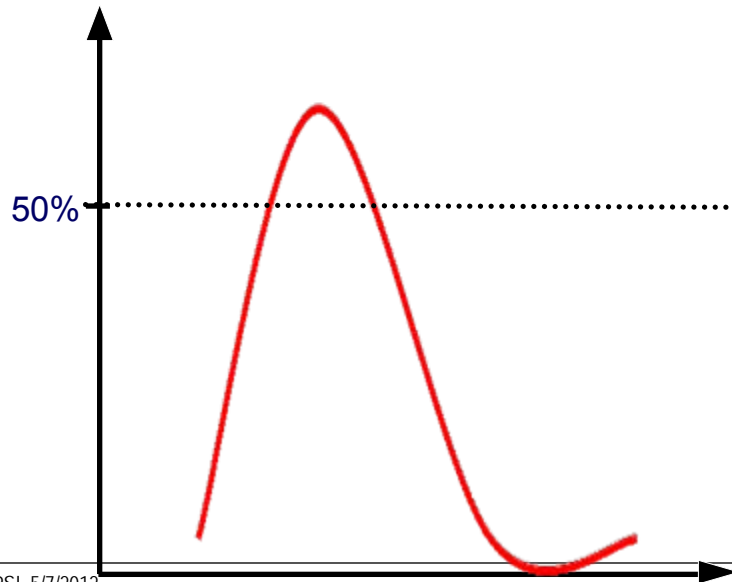
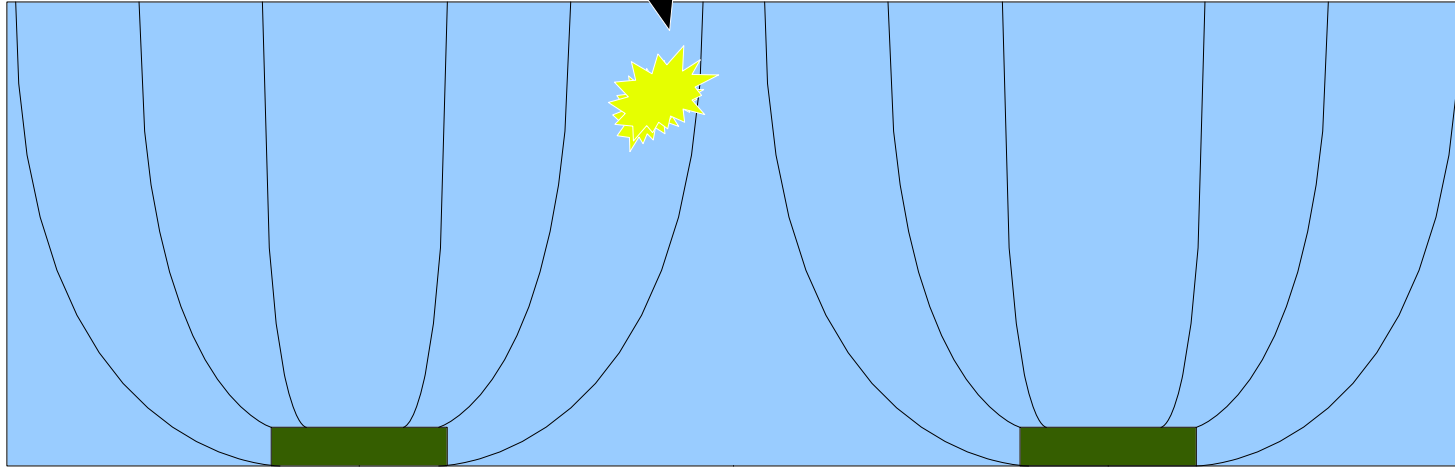
50 micron pitch, 25 keV

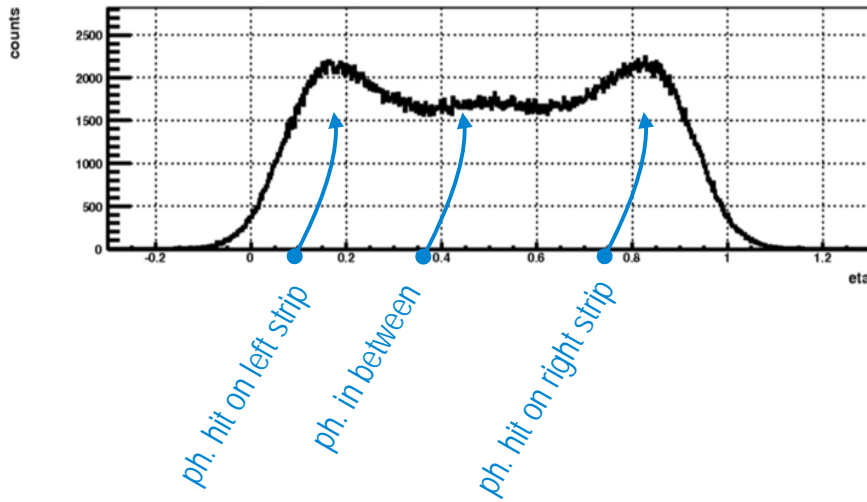


# Position dependent charge sharing



# Position dependent charge sharing

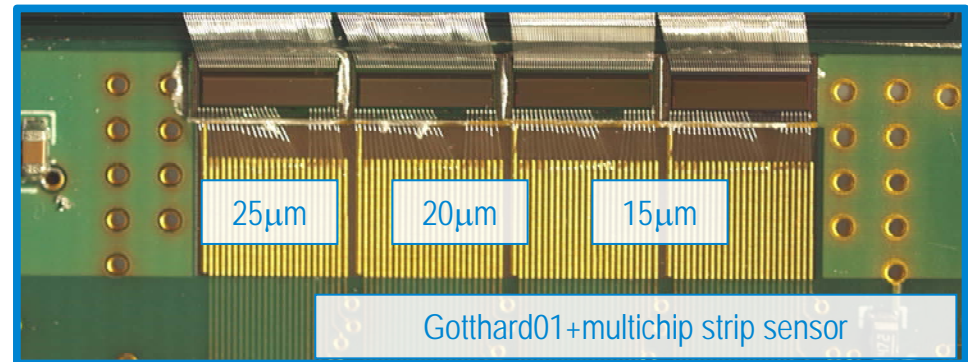
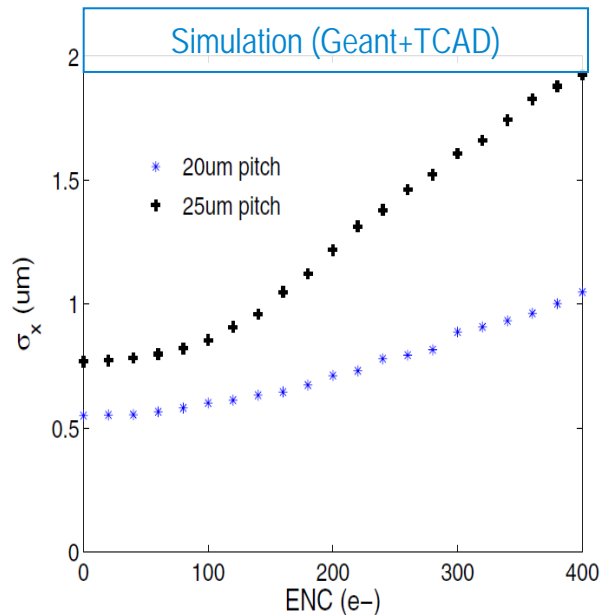




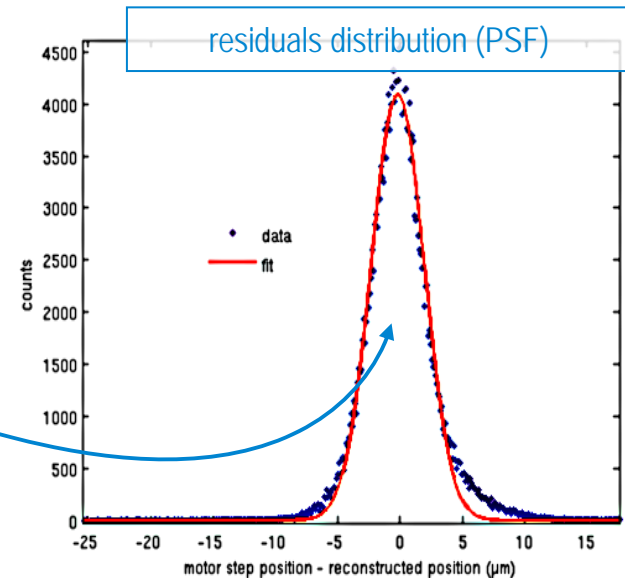
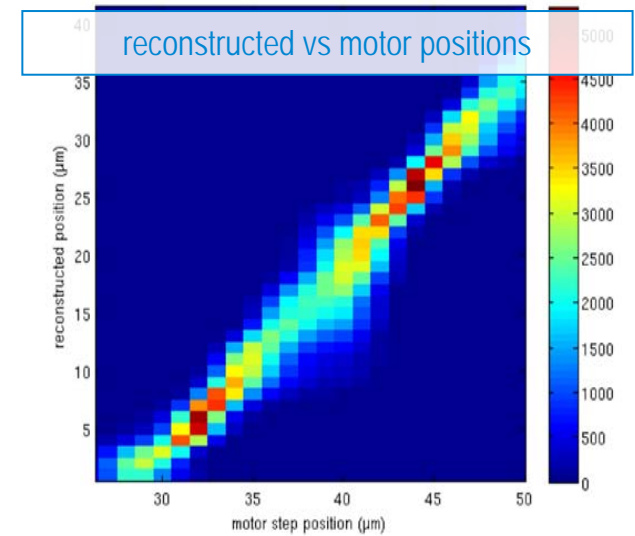
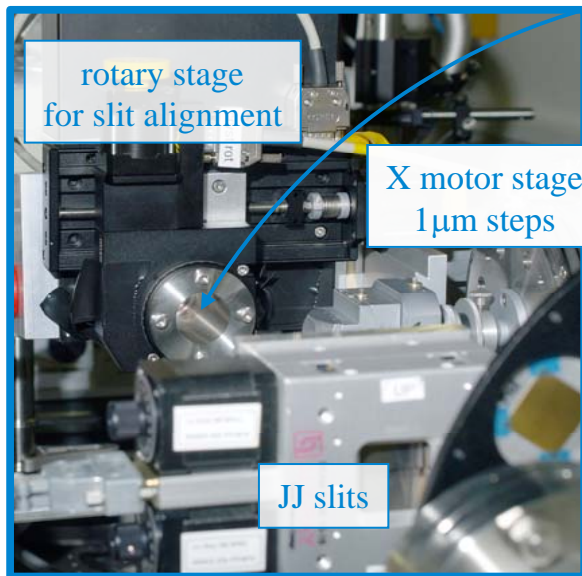
Eta algorithm for position reconstruction:

- $\eta = Q_R / (Q_L + Q_R)$
- eta distribution  $N(\eta)$  are collected for a uniform photon field
- the hit position is then:

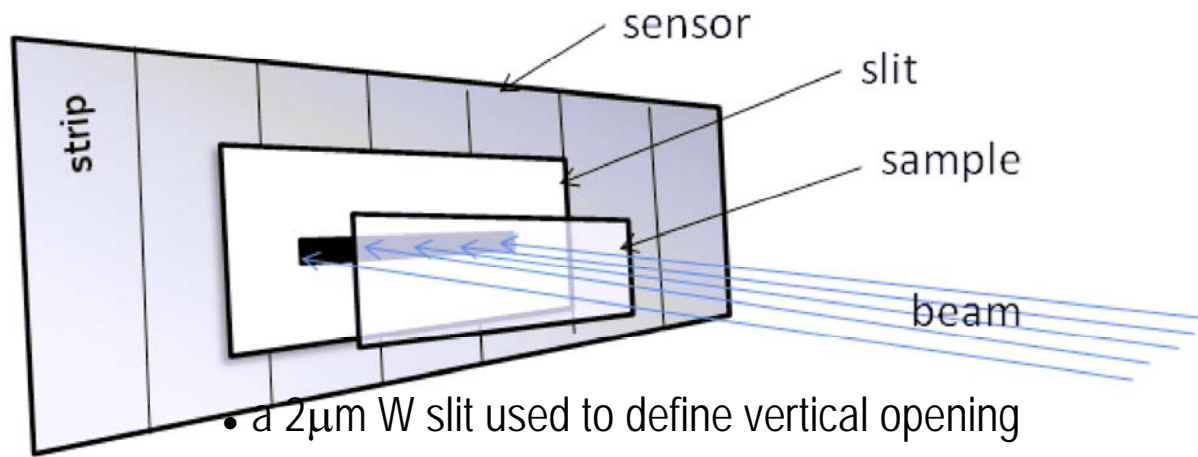
$$x_\eta = p \frac{\int_0^{\eta_0} \frac{dN}{d\eta} d\eta}{\int_0^1 \frac{dN}{d\eta} d\eta}$$



- To measure the spatial resolution a  $2\mu\text{m}$  W slit has been scanned in  $1\mu\text{m}$  steps in front of the strips
- slit parallel to strips
- Vertical beam size  $\sim 100\mu\text{m}$
- Strip pitch  $20\mu\text{m}$ ,  $15\text{keV}$  beam



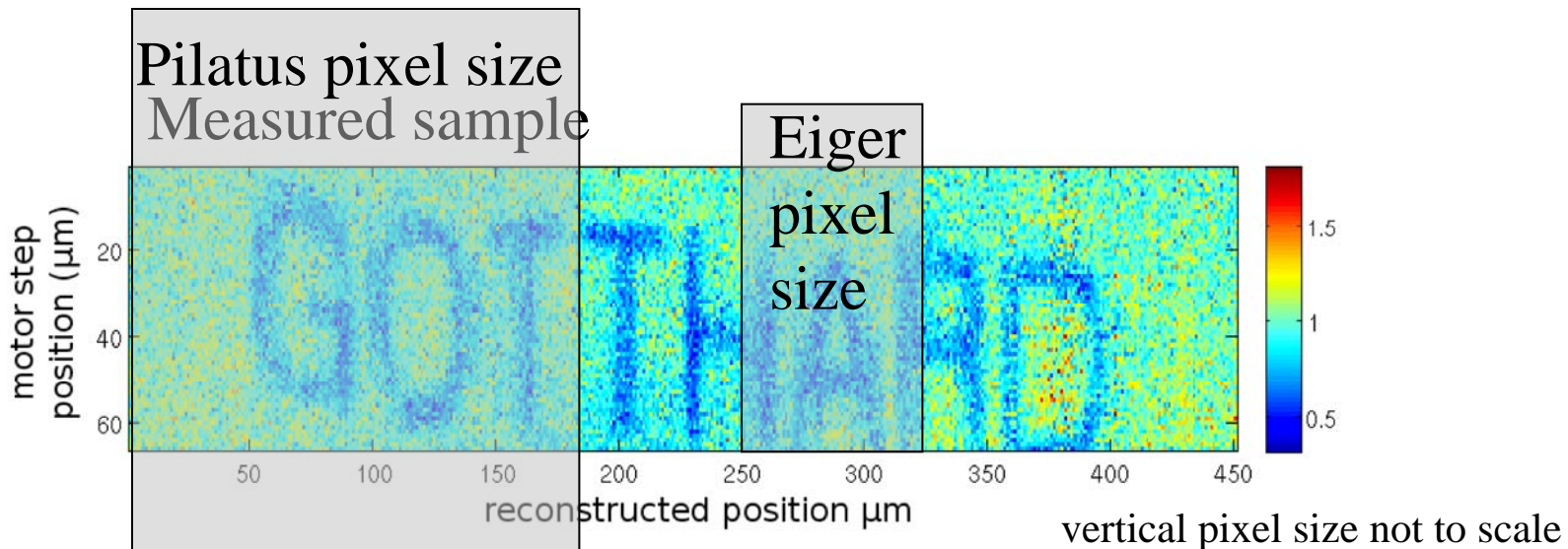
$\sigma = 1.8\mu\text{m}$



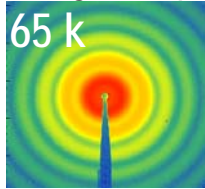
- a 2 μm W slit used to define vertical opening
- Sample scanned in 1 μm steps in front of the strips
- Strip pitch 20 μm, 15 keV beam



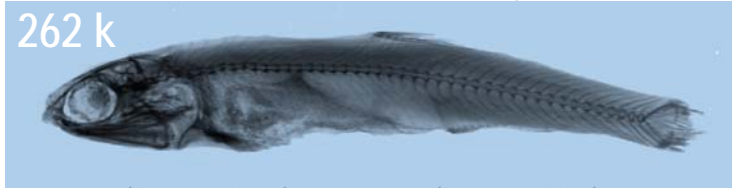
Sample 3 μm thick gold on 300 μm Si substrate  
 → little contrast!



Single chip



Half module (4 chips)



Module (8 chips)



- First results
  - characterization of the chip
  - the first demonstration experiments
  - Currently work on firmware and software, soon module working
- Single module system @ cSAXS, surface diffraction station
  - summer this year
- High performance 9M Eiger @ cSAXS
  - on board memory (288 GB RAM) or 2.5 sec @ highest data rates
  - 100 Hz continuous operation and data to disk
  - Complex IT infrastructure and data storage needed
- Eiger licensed to Dectris

- Dynamic gain switching works well
  - Single photon resolution and  $10^4$  dynamic range
  - Electronic noise below poisson fluctuations
    - single photon counting data quality
  
- Gotthard works at 1MHz frame rate in burst and 50kHz continuous
  - Well suited for diagnostics of XFELs and energy dispersive detectors
  
- Swissfel detector development just started (almost everything in hand) ready 2015
  - Based on Eiger sensor 75 micron pixel size
  - Frame rate up to 2kHz → well suited for PX (no rate limitation)
  
- Charge integrating system can have similarly low noise as single photon counting systems
  - No limit in pixel size due to charge sharing → position resolution on ~micron level can be reached

**The SLS Detector Group:  
Anna Bergamaschi, Roberto Dinapoli, Beat  
Henrich, Dominic Greiffenberg, Ian Johnson,  
Dhanya Maliakal, Aldo Mozzanica, Christian  
Ruder, Lukas Schädler, Bernd Schmitt, Xintian  
Shi**

Wir schaffen Wissen – heute für morgen

